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A formal framework for software product lines $\stackrel{\text{\tiny{trans}}}{\longrightarrow}$

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ABSTRACT

Context: A Software Product Line is a set of software systems that are built from a common set of features. These systems are developed in a prescribed way and they can be adapted to fit the needs of customers. Feature models specify the properties of the systems that are meaningful to customers. A semantics that models the feature level has the potential to support the automatic analysis of entire software product lines.

Objective: The objective of this paper is to define a formal framework for Software Product Lines. This framework needs to be general enough to provide a formal semantics for existing frameworks like FODA (Feature Oriented Domain Analysis), but also to be easily adaptable to new problems.

Method: We define an algebraic language, called SPLA, to describe Software Product Lines. We provide the semantics for the algebra in three different ways. The approach followed to give the semantics is inspired by the semantics of process algebras. First we define an operational semantics, next a denotational semantics, and finally an axiomatic semantics. We also have defined a representation of the algebra into propositional logic.

Results: We prove that the three semantics are equivalent. We also show how FODA diagrams can be automatically translated into SPLA. Furthermore, we have developed our tool, called AT, that implements the formal framework presented in this paper. This tool uses a SAT-solver to check the satisfiability of an SPL.

Conclusion: This paper defines a general formal framework for software product lines. We have defined three different semantics that are equivalent; this means that depending on the context we can choose the most convenient approach: operational, denotational or axiomatic. The framework is flexible enough because it is closely related to process algebras. Process algebras are a well-known paradigm for which many extensions have been defined.

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1. Introduction

Software Product Lines [1,2], in short SPLs, constitute a paradigm for which industrial production techniques are adapted and applied to software development. In contrast to classical techniques, where each company develops its own software product, SPLs define generic software products, enabling mass customization [3]. Generally speaking, using SPLs provide a systematic and disciplined approach to developing software. It covers all aspects of the software production cycle and requires expertise in data management, design, algorithm paradigms, programming languages, and human-computer interfaces.

When developing SPLs, it is necessary to apply sound engineering principles in order to obtain economically reliable and efficient

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software. *Formal methods* [4–9] are useful for this task. A formal method is a set of mathematical techniques that allows automated design, specification, development and verification of software systems. For this process to work properly, a well defined formalism must exist. There are many formalisms to represent SPLs [10–13]. We focus on one of the most widely approaches: *Feature models* [10,13].

A feature model is a compact representation of all the products of an SPL in terms of commonality and variability. Generally these features are related using a tree-like diagram. A variation point is a place where a decision can be made to determine if none, one, or more features can be selected to be part of the final product. For instance, in these models we can represent the following property:

There exists a product with features A and C.

In addition, it is easy to represent constraints over the features in feature models. For instance, we could represent the following property:

In any valid product, if feature ${\tt C}$ is included then features ${\tt A}$ and ${\tt B}$ must also be included.







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Feature Oriented Domain Analysis [10], in short FODA, is a feature model to represent SPLs. This model allows us to graphically represent *features* and their *relationships*, in order to define *products* in an SPL. The graphical structure of a FODA model is represented by a FODA *Diagram*. A FODA Diagram is essentially an intuitive and easy to understand graph where there is relevant information about the features. This diagram has two different elements: the set of nodes and the set of arcs. The former represents the features of the SPL. The latter represents the relationships and the constraints of the SPL. We introduce the basic components of a FODA diagram in Fig. 1. With these elements we can model complex SPLs.

For instance, let us look at the FODA Diagrams in Fig. 2. The Examples **a** and **b** show two SPLs with possibly two possible features A and B. With respect to **a**, the feature A will appear in all valid products of this SPL, while B is optional. Therefore, the valid set of products of this FODA Diagram is one product with A and one product with features A and B. In **b** both features are mandatory, i.e. any product generated from this SPL will contain features A and B.

Example **c**, represents an SPL with a *choose-one* operator. There are three different features: A, B and C in this diagram. Any valid product of **c** will contain A and one of these features B or C. The *conjunction* operator is shown in **d** and **e**. In both examples two branches leave feature A. On the one hand, the branches in Example **d** are mandatory. This means that there is only one product derived from this diagram: the one that contains features A, B, and C. On the other hand, one branch in Example **e** is optional and the other is mandatory. This means that there are two products derived from this diagram: one with features A and C, and one with features A, B, and C.

Finally, more complex properties appear in Examples **f** and **g**. In these diagrams there are tree constraints combined with optional features. In **f**, there is an *exclusion* constraint: If B is included in a product then feature C cannot appear in the same product. In **g**, there is a *require* constraint: If B is included then C must also be included.

Although FODA Diagrams are very intuitive, sometimes it can be hard to analyze all the restrictions and the relationships between

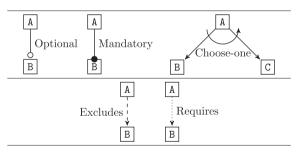


Fig. 1. FODA diagram representation.

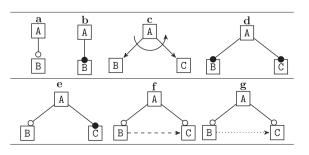


Fig. 2. Examples of FODA diagrams.

features. In order to make a formal analysis we have to provide a *formal semantics* for the diagrams. To obtain a formal semantics for SPLs, first we need a formal language. In this paper we define a formal language called SPLA. As we will see in Section 3, all FODA Diagrams can be automatically translated into SPLA.

After presenting SPLA, we need to introduce the formal semantics of this algebra. There is previous work on formalizing FODA and feature models [30,22,31–33,23,24] that we briefly review in the next section. The approach we follow in this paper is inspired by classical process algebras [4,6,5]. We define three different semantics for the language. First we introduce an operational semantics whose computations give the products of an SPL. Next we define a denotational semantics that is less intuitive but easier to implement. Finally we have defined an axiomatic semantics. We prove that all three semantics are equivalent to each other.

In addition to presenting the formal framework, we have developed a tool called AT. This tool is an implementation of the formal semantics presented in this paper. Using AT it is possible to check properties such as:

- Can this SPL produce a valid product?
- How many valid products can we build within this SPL?
- Given an SPL diagram, can we generate an equivalent SPL diagram with fewer restrictions than the first one?

This tool is completely implemented in JAVA. This tool has a module to check the satisfiability of an SPL diagram. We carry out some experiment using diagrams obtained from the random SPL diagram generator Betty. These experiments shows the scalability of AT. We have checked satisfiability of diagrams with 13,000 features; such diagrams are relatively large given the state of the art.

In this paper we present a syntactic and semantic framework that formalizes FODA-like diagrams. First we present a syntax with the basic operators presented in a FODA-like diagram. Next we define an operational semantics. This semantics is intuitive and it captures the notions of the operators. After the operational semantics, we give the denotational semantics. This semantics is more appropriate for obtaining the products of an SPL. We prove that both semantics are equivalent. We also define an axiomatic semantics. As far as we know, this semantics does not appear in any of the previous frameworks. We prove that this semantics is sound and complete with respect to the previous ones. Inspired by recent works in process algebras, we also give a way to represent the terms of the algebra into propositional logic. Finally we have implemented a tool that supports our framework. The tool is split into two modules. One module deals with the denotational and axiomatic semantics while the other deals with the representation into propositional logic. The second module uses SAT-solver to check the satisfiability of an SPL.

This paper tries to show that SPLs can benefit from the process algebra community mainly because process algebras have been studied from many points of view. For instance, there have been numerous proposals to incorporate non-functional aspects such as time and probabilities. In particular, we are currently working in the following aspects. First, we are studying how to introduce, the notions of costs and time. In this context, it is also important the order in which products are elaborated, and therefore, sequences instead of sets have to be used. Second, we would like to work with models that indicate the probability of a product. This can be applied, for example, in software testing, so that we can add more resources to test the products with higher probabilities. Moreover, our semantic approach, based on alternative semantics that are shown to be equivalent, can be used in further extensions of both the formalism used in this paper and other formalisms of similar nature.

The paper is structured as follows. In Section 2 we review different formal models used to represent SPLs and compare other algebraic approaches with the one presented in this paper. In Section 3 we show the full syntax of our algebra and how any FODA Diagram is translated into the terms of SPLA. In Sections 4–6 the operational, denotational and the axiomatic semantics (respectively) of SPLA are presented. Next, in Section 8 we show the applicability of this approach in a complete study of a video streaming system. In Section 9 we present some features of our tool AT, that implements our formal framework. Finally, we conclude this paper in Section 10, by presenting some conclusions and presenting possible lines of future work. In addition to these sections, there is an Appendix with all the proofs of the results of this paper.

2. Related work

In this section we present the most usual formal frameworks used to model SPLs. Let us note that formal methods use both a formal *syntax* and a formal *semantics*. The former is used to write a precise specification of a system. With the latter a precise meaning is given for each description in the language.

Several proposals exist to formally describe SPLs. We have classified these formal frameworks into two categories. On the one hand, there are proposals that adapt well-known formal frameworks, like transition systems [14–21], to represent SPLs. On the other hand, there are proposals that give a semantics to originally informal frameworks dealing with SPL, such as Feature Models [22–24].

Next we are going to discuss some frameworks that are in the first category. First we describe frameworks [14–18] that deal with adaptations of transition systems. The most widely used model is the Modal Transition System, in short MTS. An MTS is an extension of a Labelled Transition System. In MTSs, there are two types of transitions: obligatory and optional, representing transitions that are always performed and transitions that may be included or discarded respectively. Furthermore, they present different notions of conformance between sets of products. Let us note that the original MTS models did not have the capability to define constraints between features. This problem was overcome with the Extended Modal Transition Systems [15] and by an associated set of logical formulae [19,21]. There are other studies that have been able to extend the functionality of MTSs. In particular, the authors in [16] define the Modal I/O Automata to represent the behavior of SPLs. Finally, it is worth mentioning another approach that models SPLs based on Petri Nets [25].

There are other frameworks that follow an algebraic approach [26–28]. In mathematical terms an *algebra* consists of a set of symbols denoting *values* of some type, and *operations* on the values. In [26,27] the authors define the SPLs as idempotent semirings where the values are the features. The denotational semantics presented in this paper is closely related to this model. Gruler et al. [28] present an algebra based on the classical CCS [4] process algebra called PL-CCS. This framework is also closely related to ours. Later we describe how our approach relates to theirs. Finally, in [29] SPLs are represented as logical expressions.

With regards to the second category, we can mention the following [30,22,31–33,23,24]. In these frameworks authors provide a semantics for existing feature models [34,10,35,36] such as FODA and its extension RSEB. Benavides et al. [23] make an automated analysis of feature models. In [22] the author uses the model defined in [29] to model FODA. In [30] and its extension [32] the authors define a semantics for FODA based on what they call a *tree feature diagram*. [24] deserves special mention since the authors present a general framework where they can define semantics for all graph-like diagrams like FODA. The authors in [33] translate a feature diagram into propositional logic. In this way they can verify the consistency of models of at least 10,000 features.

In our framework, we present a process algebra to represent SPLs. In this sense our approach is a new formalism to represent SPLs, so it lies in the first category. One of the main objectives of our proposal is to define a general framework that can be used to provide FODA, or other graph-like diagrams, with a formal semantics that removes any ambiguity or lack of precision as in [32,24]. Therefore our approach also lies in the second category. The approach given by our semantics is similar to the one in [28], but our intention has not been to extend an existing process algebra but to define an algebra to represent graph-like diagrams with cross tree constraints, like FODA. In this sense our approach is simpler and the semantics is also simpler. In particular, we do not need the fixed point theory because there is no recursion in the graph-like diagrams we study. However, it would have been possible to include a recursion-like operator. Since our semantics is based on traces, it is likely that we can define a complete partial order in our semantics so that all the operators are continuous. The algebraic approach in [26,27] is similar to our denotational semantics. They present a semiring with two operators that correspond to our choose-one and conjunction operators, and two constants that correspond to our *r* and nil constants. They present neither an operational nor an axiomatic semantics. Let us note that one advantage of our approach is that it is based on the well-known formalism of process algebras. This formalism is very flexible and many extensions of process algebras have been studied to incorporate non-functional features like time and probabilities. we anticipate that this will allow us to incorporate new characteristics in the future (see Section 10).

To conclude this section we would like to mention that SATsolvers have been used recently in the context of process algebras [37–39].

3. FODA algebra

In this section we present the syntax of our algebra and how FODA Diagrams are translated into this algebra. First, in sub section 3.1 we define the syntax of the algebra and we explain the intuitive meaning of the operators. Next, in Section 3.2 we introduce the translation of a FODA Diagram into our syntax.

3.1. Syntax of FODA

The *syntax* concerns the principles and rules for constructing *terms*. We define the language SPLA by means of an Extended BNF expression. In order to define the syntax, we need to fix the set of *features*. From now on \mathcal{F} denotes a finite set of features and A, B, C, etc. denote isolated features.

In the syntax of the language there are two sets of operators. On the one hand there are *main operators*, such as $\cdot \vee \cdot, \cdot \wedge \cdot, A; \cdot, \overline{A}; \cdot, \overline{A} \Rightarrow \mathbb{B} \text{ in } \cdot, A \Rightarrow \mathbb{B} \text{ in } \cdot, A \Rightarrow \mathbb{B} \text{ in } \cdot, \text{ that directly correspond to relationships}$ in FODA Diagrams. On the other hand, we have *auxiliary operators*, such as nil, \checkmark , $\cdot \setminus A, \cdot \Rightarrow A$, which we need to define the semantics of the language.

Definition 1. A *software product line* is a term generated by the following Extended BNF-like expression:

$$\begin{array}{ll} P ::= & \checkmark |\texttt{nil}|\mathbb{A}; P|\overline{\mathbb{A}}; P| \\ & P \lor Q|P \land Q|\mathbb{A} \Rightarrow \mathbb{B} \text{ in } P| \\ & \mathbb{A} \Rightarrow \mathbb{B} \text{ in } P|P \setminus \mathbb{A}|P \Rightarrow \mathbb{A} \end{array}$$

where $A,B\in \mathcal{F}.$ We denote the set of terms of this algebra by <code>SPLA</code>.

In order to avoid writing too many parentheses in the terms, we are going to assume left-associativity in binary operators and the following precedence in the operators (from higher to lower priority): $A; P, \overline{A}; P, P \lor Q, P \land Q, A \Rightarrow B \text{ in } P, A \Rightarrow B$

The Extended BNF in Definition 1 says that a term of SPLA is a sequence of operators and features. An SPLA term represents sets of *products*. We introduce the formal definition of products of an SPL expressed in SPLA in Section 4. Basically, a product is a set of features that can be derived from an SPLA term. Next we explain the meaning of each operator by using some examples.

There are two terminal symbols in the language, nil and \checkmark , we need them to define the semantics of the language. Let us note that the products of a term in SPLA will be computed following some rules. The computation will finish when no further steps are allowed. This fact is represented by the nil symbol. We will introduce rules to compute a product, with this computation finishing when no further steps are required, a situation represented by nil During the computation of an SPLA term, we have to represent the situation in which a *valid product* of the term has been computed. This fact is represented by the \checkmark symbol.

The operators A;P and $\overline{A};P$ add the feature A to any product that can be obtained from P. The operator A;P indicates that A is mandatory while $\overline{A};P$ indicates that A is optional. There are two binary operators: $P \lor Q$ and $P \land Q$. The first one represents the *choose-one* operator while the second one represents the *conjunction* operator.

Example 1. The term $A; \overline{B}; \checkmark$ represents an SPL with two valid products. We have a product with only the feature A and another product with the features A and B. This is because feature B is optional in this case.

The term A; $\checkmark (B$; $\checkmark C$; $\checkmark)$ has three valid products with one feature in each. The first has feature A, the second has feature B and the third has feature C.

We will show that \lor is commutative and associative so we could rewrite the previous term without parentheses: A; $\checkmark \lor B$; $\checkmark \lor C$; \checkmark . Therefore, this operator can be seen as choosing 1 feature from *n* options.

The term $A; (B; \not \sim \land C; \not \sim)$ represents a mandatory relationship; and we will see that this term has only one product with three features: A, B, and C. As well as the *choose-one* operator, we will show that the \land operator is commutative and associative. So we can consider this an *n*-ary operator.

The constraints are easily represented in SPLA. The operator $A \Rightarrow BinP$ represents the *require* constraint in FODA. The operator $A \Rightarrow B$ in *P* represents the *exclusion* constraint in FODA.

Example 2. The term $A \Rightarrow B \text{ in } A$; \checkmark has only one valid product with the features A and B.

Let us consider P = A; (B; $\checkmark \lor C$; \checkmark). This term has two valid products: The first one has the features A and B, while the second one has the features A and C.

If we add to the previous term the following constraint $A \Rightarrow BinP$, then this new term has only one product with the features A and C.

The operator $P \Rightarrow A$ is necessary to define the behavior of the $A \Rightarrow B in P$ operator: When we compute the products of the term $A \Rightarrow B$ in P, we have to take into account whether product A has been produced or not. In the case it has been produced, we have to annotate that we need to produce B in the future. The operator $P \Rightarrow B$ is used for this purpose. The same happens with the operator

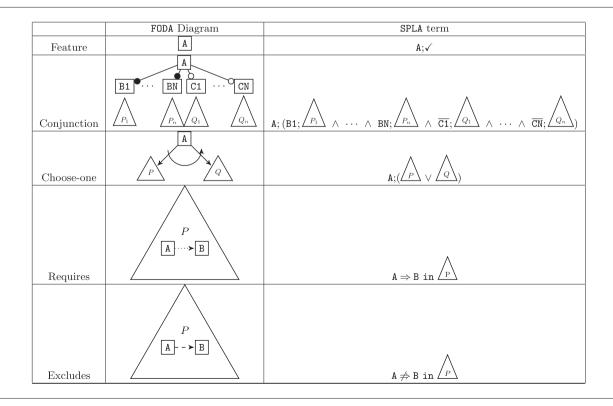


Fig. 3. Mapping from FODA diagram to SPLA.

 $P \setminus \mathbb{B}$. When we compute the products of $\mathbb{A} \Rightarrow \mathbb{B}$ in P, if the feature \mathbb{A} is computed at some point, we annotate that \mathbb{B} must not be included. The operator $P \setminus \mathbb{B}$ indicates that product \mathbb{B} is forbidden.

3.2. Translation: from FODA to SPLA

The matching table used to translate FODA diagrams to SPLA syntax is presented in Fig. 3. Any FODA Diagram can be translated into SPLA by using the rules in this figure. The translation from FODA into SPLA is made in three steps:

- 1. First we make the translation of the diagram without taking into consideration any kind of restrictions.
- 2. Next we codify the *require* restrictions. The order in which these relations appear may be relevant. In order to overcome this problem, we introduce all the restrictions that are in the transitive closure of the original diagram: If the *require* constraints A......> B and B......> C are in the diagram, we also introduce the *require* constraint A.....> C. Proposition 9 proves that, if the set of constraints are closed under transitivity then the order in which they are chosen is not important. Let us remark that this closure can be computed in O(n³) with the Floyd–Warshall algorithm, with *n* being the number of involved features.
- 3. Finally we codify the *exclude* constraints. In this case Proposition 4 proves that the order in which the *exclude* constraints are chosen is not important.

This translation is correct because of Propositions 9 and 4. These propositions cannot be included here because they need the equivalence relation that will be introduced in Section 4 (Definition 6). Furthermore, their proof is easier after the results about the denotational semantics in Section 5.

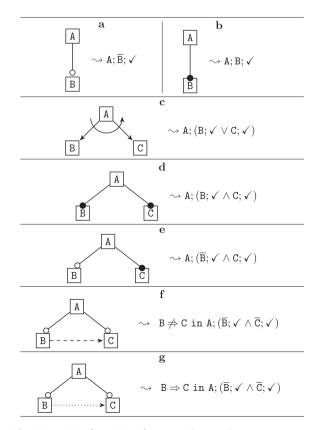


Fig. 4. Examples of translation from FODA diagrams into SPLA grammar.

In order to add clarity, Fig. 3 only considers choose-one diagrams with two choices. However, we can represent also *n*-ary *choose-one* diagrams because, as we have already said, the \lor operator in SPLA is commutative and associative (Proposition 2).

Example 3. The translation of the FODA Diagram in Fig. 2 by using the mapping rules of Fig. 3 is presented in Fig. 4.

4. Operational semantics

So far, we only have a syntax to express the SPLs in SPLA, but for a formal study we need a semantics. In this section we define a labeled transition system for any term $P \in \text{SPLA}$. The transitions are *annotated* with the set $\mathcal{F} \cup \{\mathcal{V}\}$, with \mathcal{F} being the set of features and $\mathcal{V} \notin \mathcal{F}$. In particular, if $A \in \mathcal{F}$, the transition $P \stackrel{A}{\to} Q$ means that there is a product of P that contains the feature A. The transitions of the form $P \stackrel{\leftarrow}{\to} \text{nil}$ mean that a product has been produced. The formal operational semantic rules of the algebra are presented in Fig. 5.

Definition 2. Let $P, Q \in SPLA$ and $a \in \mathcal{F} \cup \{\mathcal{V}\}$. There is a transition from P to Q labeled with the symbol a, denoted by $P \xrightarrow{a} Q$, if it can be deduced from the rules in Fig. 5.

Before giving any properties of this semantics let us justify the rules in Fig. 5. We will define the products of an SPL from the set of traces obtained from the defined transitions.

First we have the rule [**tick**]. The intuitive meaning of this rule is that we have reached a point where a product of the SPL has been computed. Let us note that nil has no transitions, this means that nil does not have any valid products.

Rules [**feat**], [**ofeat**1], and [**ofeat**2] deal directly with the computation of features. Rule [**ofeat**2] means that we have a valid product without considering an optional feature, in other words, this rule is the one that establishes the difference between an optional and a mandatory feature. Feature A is optional in *P* because $P \stackrel{A}{\rightarrow} P_1$ and $P \stackrel{\checkmark}{\rightarrow} nil$. In this sense, the transition $P \stackrel{\leftarrow}{\rightarrow} nil$ indicates not only that *P* has already computed a valid product, but also it indicates that if *P* can compute any other features, these additional features are optional.

Rules **[cho1]** and **[cho2]** deal with the *choose-one* operator. These rules indicate that the computation of $P \lor Q$ must choose between the features in P or the features in Q.

Rules [**con**1], [**con**2], and [**con**3] deal with the *conjunction* operator. The main rules are [**con**1] and [**con**2]. These rules are symmetrical to each other. They indicate that any product of $P \land Q$ must have the features of P and Q. Rule [**con**3] indicates that both members have to agree in order to *deliver* a product.

Rules [**req1**], [**req2**], and [**req3**] deal with the *require* constraint. Rule [**req1**] indicate that $A \Rightarrow B \text{ in } P$ behaves like P as long as feature A has not been computed. Rule [**req2**] indicates that B is mandatory once A has been computed. Finally [**req3**] is necessary for Lemma 1.

Rules [**excl1**] to [**excl4**] deal with the exclusion constraint. Rule [**excl1**] indicates that $A \Rightarrow B \text{ in } P$ behaves like P as long as P does not compute feature A or B. Rule [**excl2**] indicates that once P produces A, feature B must be forbidden. Rule [**excl3**] indicates just the opposite: when feature B is computed, then A must be forbidden. This rule might be surprising, but there is no reason to forbid $A \Rightarrow B \text{ in } P$ to compute feature B. So if $A \Rightarrow B \text{ in } P$ computes feature B then feature A must be forbidden. Otherwise the exclusion constraint would not have been fulfilled.

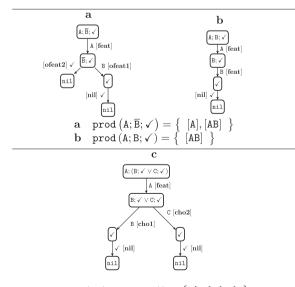
Rules [**forb1**] and [**forb2**] deal with the auxiliary operator $P \setminus A$ that forbids the computation of feature A. Let us note that there is no rule that computes A. This means that if feature A is computed by *P*, the computation is blocked and no products can be produced.

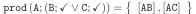
[tick]	$\checkmark \xrightarrow{\checkmark}$ nil	[feat]	$\mathtt{A}; P \xrightarrow{\hspace{0.1cm} \mathtt{A} \hspace{0.1cm}} P$
[ofeat1]	$\overline{\mathbf{A}}; P \xrightarrow{\mathbf{A}} P$	[ofeat2]	$\overline{\mathtt{A}}; P \xrightarrow{\checkmark} \mathtt{nil}$
[cho1]	$\frac{P \xrightarrow{a} P_1}{P \lor Q \xrightarrow{a} P_1}$	[cho2]	$\frac{P \xrightarrow{a} P_1}{Q \lor P \xrightarrow{a} P_1}$
$[\mathbf{con1}]$	$\frac{P \xrightarrow{A} P_1}{P \land Q \xrightarrow{A} P_1 \land Q}$	$[\mathbf{con2}]$	$\frac{P \xrightarrow{\mathbb{A}} P_1}{Q \wedge P \xrightarrow{\mathbb{A}} Q \wedge P_1}$
$[\mathbf{con3}]$	$\frac{P \xrightarrow{\checkmark} \operatorname{nil}, Q \xrightarrow{\checkmark} \operatorname{nil}}{P \land Q \xrightarrow{\checkmark} \operatorname{nil}}$	[req1]	$\frac{P \stackrel{c}{\longrightarrow} P_1, \ C \neq A}{A \Rightarrow B \text{ in } P \stackrel{c}{\longrightarrow} A \Rightarrow B \text{ in } P_1}$
[req2]	$\frac{P \xrightarrow{A} P_1}{A \Rightarrow B \text{ in } P \xrightarrow{A} P_1 \Rightarrow B}$	[req3]	$\frac{P \stackrel{\checkmark}{\longrightarrow} \text{nil}}{A \Rightarrow B \text{ in } P \stackrel{\checkmark}{\longrightarrow} \text{nil}}$
	$\begin{array}{c} A \xrightarrow{c} B \text{ in } P \xrightarrow{c} P_1, \ C \neq A \land C \neq B \\ A \neq B \text{ in } P \xrightarrow{c} A \neq B \text{ in } P_1 \end{array}$	[excl2]	$ \frac{P \xrightarrow{A} P_1}{A \not\Rightarrow B \text{ in } P \xrightarrow{A} P_1 \setminus B} $
[excl3]	$\frac{P \xrightarrow{B} P_1}{A \neq B \text{ in } P \xrightarrow{B} P_1 \setminus A}$	[excl4]	$\frac{P \stackrel{\checkmark}{\longrightarrow} \text{nil}}{A \neq B \text{ in } P \stackrel{\checkmark}{\longrightarrow} \text{nil}}$
[forb1]	$\frac{P \xrightarrow{B} P_1, \ B \neq A}{P \backslash A \xrightarrow{B} P_1 \backslash A}$	$[\mathbf{forb2}]$	$\frac{P \xrightarrow{\checkmark} \text{nil}}{P \backslash A \xrightarrow{\checkmark} \text{nil}}$
[mand1]	$\frac{P \xrightarrow{\checkmark} \text{nil}}{P \Rightarrow \mathbf{A} \xrightarrow{A} \mathbf{\checkmark}}$	[mand2]	$] \qquad \frac{P \xrightarrow{A} P_1}{P \Rightarrow A \xrightarrow{A} P_1}$
[mand3]	$\frac{P \xrightarrow{B} P_1, \ \mathbf{A} \neq \mathbf{B}}{P \Rightarrow \mathbf{A} \xrightarrow{B} P_1 \Rightarrow \mathbf{A}}$		
	$\mathtt{A}, \mathtt{B}, \mathtt{C} \in \mathcal{F},$	$a \in \mathcal{F} \cup \{$	$\{\checkmark\}$

Fig. 5. Rules defining the operational semantics of SPLA.

Rules [mand1], [mand2], and [mand3] deal with the auxiliary operator $P \Rightarrow A$. Rule [mand1] indicates that feature A must be computed before *delivering* a product. Rules [mand2] and [mand3] indicates that $P \Rightarrow A$ behaves like *P*. We need two rules in this case because when feature A is computed it is no longer necessary to continue considering this feature as mandatory and so the operator can be removed from the term.

We can see the operational semantics of a term as a *computational* tree (see the examples in Figs. 6–8). The root is the term itself and the branches are labeled with features. The branches of the tree represent the products of the term. We obtain a valid product when we reach a node that has an outgoing arc labeled with \checkmark . At





 $\begin{array}{c} d\\ \hline A; (B; \checkmark \land C; \checkmark)\\ \hline A \text{ [feat]}\\ \hline B; \checkmark \land C; \checkmark \\ \hline C \text{ [con2]}\\ \hline C \text{ [con1]}\\ \hline \downarrow \land \land \checkmark \\ \hline (con3)\\ \hline n11 \\ \hline n11 \\ \end{array}$

$$\operatorname{prod}\left(\mathsf{A};\left(\mathsf{B};\checkmark\wedge\mathsf{C};\checkmark\right)\right) = \left\{ \left[\operatorname{ABC}\right] \right\}$$

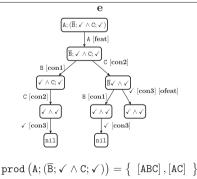


Fig. 7. Application of the operational semantic rules 2/3.

this point no more features can be obtained in the corresponding branch. This is what the following lemma establishes.

Lemma 1. Let $P, Q \in \text{SPLA}$, if $P \xrightarrow{\checkmark} Q$ then Q = nil.

Once we have defined the operational semantics of the algebra, we can define the traces of an SPL, and from these traces we obtain its products.

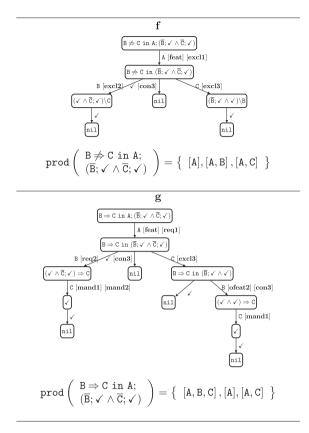


Fig. 8. Application of the operational semantic rules 3/3.

Definition 3. A trace is a sequence $s \in \mathcal{F}^*$. The empty trace is denoted by ϵ . Let s_1 and s_2 be traces, we denote the concatenation of s_1 and s_2 by $s_1 \cdot s_2$. Let $A \in \mathcal{F}$ and let s be a trace, we say that A *is in the trace s*, written $A \in s$, iff there exist traces s_1 and s_2 such that $s = s_1 \cdot \mathbb{A} \cdot s_2$.

We can extend the transitions in Definition 2 to traces. Let P, Q, $R \in SPLA$, we inductively define the transitions $P \xrightarrow{s} R$ as follows:

- $P \xrightarrow{\epsilon} P$.
- If $P \xrightarrow{A} Q$ and $Q \xrightarrow{s} R$, then $P \xrightarrow{A \cdot S} R$.

Only traces ending with the symbol \nvdash can be considered as products. Therefore, in order to obtain the products of an SPL, we need to take its successful traces: the traces that end with the \checkmark symbol. Let us note that the \checkmark symbol is not a feature, so we do not include it in the trace.

Definition 4. Let $P \in SPLA$ and $s \in \mathcal{F}^*$, s is a successful trace of P, written $s \in tr(P)$, iff $P \xrightarrow{s} Q \xrightarrow{\checkmark} nil$.

The order in which the features are produced cannot be represented in FODA. For this reason, different traces can define the same product. For instance, the product obtained from the trace AB is the same as the one represented by the trace BA. Thus in order to get the products of an SPL we have to consider sets that result from traces.

Definition 5. Let *s* be a trace. The set induced by the trace, written [s], is the set obtained from the elements of the trace without considering their position in the trace.

Let $P \in SPLA$, we define the products of P, written prod(P), as $\operatorname{prod}(P) = \{ [s] | s \in \operatorname{tr}(P) \}.$

Example 4. Now let us consider the SPLA term P = A; $(\overline{\mathbb{B}}; \boldsymbol{\checkmark} \wedge \overline{\mathbb{C}}; \boldsymbol{\checkmark})$. The possible computations of *P* are:

$$P \stackrel{A}{\rightarrow} \overline{B}; \checkmark \land \overline{C}; \checkmark \stackrel{B}{\rightarrow} nil$$

$$P \stackrel{A}{\rightarrow} \overline{B}; \checkmark \land \overline{C}; \checkmark \stackrel{B}{\rightarrow} \checkmark \land \overline{C}; \checkmark \stackrel{L}{\rightarrow} nil$$

$$P \stackrel{A}{\rightarrow} \overline{B}; \checkmark \land \overline{C}; \checkmark \stackrel{B}{\rightarrow} \checkmark \land \overline{C}; \checkmark \stackrel{C}{\rightarrow} nil$$

$$P \stackrel{A}{\rightarrow} \overline{B}; \checkmark \land \overline{C}; \checkmark \stackrel{C}{\rightarrow} \overline{B}; \checkmark \land \checkmark \stackrel{L}{\rightarrow} nil$$

$$P \stackrel{A}{\rightarrow} \overline{B}; \checkmark \land \overline{C}; \checkmark \stackrel{C}{\rightarrow} \overline{B}; \checkmark \land \checkmark \stackrel{L}{\rightarrow} nil$$

Then $tr(P) = \{A, AB, ABC, AC, ACB\}$. Therefore $prod(P) = \{[A], [A-P], [A-P],$ B], [ABC], [AC] | since [ABC] = [ACB]

In order to illustrate the operational semantics, we will review the examples presented in Fig. 4, giving their semantics.

Example 5. The semantics of the terms in Fig. 4 have been split in Figs. 6-8.

First let us discuss the differences between examples **a** and **b**. In example **a** feature \mathbb{B} is optional while in **b** it is mandatory. This is reflected in example **b** by the fact that there is a branch corresponding to the transition $\overline{\mathbb{B}}$; $\mu \stackrel{\checkmark}{\rightarrow} \text{nil}$. This branch is not in Example **a**.

Now let us focus on Examples **c** and **d**. They show the difference between the conjunction operator and the choose-one operator. In the choose-one operator the member that is not needed for the computation disappears, while in the conjunction operator the other member remains. As a result Example **c** has the traces AB and AC, giving two different products [AB] and [AC]. In contrast, the traces in example **d** are ABC and ACB, giving just the product [ABC].

Once we have defined the products of an SPL, it is the time to define an equivalence relation based on products.

Definition 6. Let $P, Q \in SPLA$. We say that P and Q are equivalent, written $P \equiv Q$ if the products derived from both SPLs are the same: prod(P) = prod(Q).

Since the relation \equiv is based on set equality it is also an equivalence relation. In Section 5 we will see that it is also a congruence.

Proposition 1. Let $P, Q, R \in SPLA$. The following properties hold:

• $P \equiv P$.

• If
$$P \equiv Q$$
 then $Q \equiv F$

• If $P \equiv Q$ then $Q \equiv P$. • If $P \equiv Q$ and $Q \equiv R$ then $P \equiv R$.

Next we present some basic properties of the algebra, such as the commutativity and associativity of the binary operators. These properties are important since they allow us to extend the binary operators to *n*-ary operators.

Proposition 2. Let P, Q, $R \in SPLA$. The following properties hold:

Commutativity
$$P \lor Q \equiv Q \lor P$$
 and $P \land Q \equiv Q \land P$.
Associativity $P \lor (Q \lor R) \equiv (P \lor Q) \lor R$ and $P \land (Q \land R) \equiv (P \land Q) \land R$.

5. Denotational semantics

The *denotational* semantics is more abstract than the operational one, since it does not rely on computation steps. In this section we provide a denotational semantics for SPLA. In order to define this denotational semantics, the first thing to do is to establish the mathematical domain where the syntactical objects of SPLA will be represented.

As we have noted in Section 3, the semantics of any SPLA expression is given by its set of products, and each product can be characterized by its features. So the mathematical domain we need is $\mathcal{P}(\mathcal{P}(\mathcal{F}))$,¹ remembering that \mathcal{F} is the set of features. The next step is to define a semantic operator for any of the syntactical operators in SPLA. This is done in the following definition.

Definition 7. Let $P, Q \in \mathcal{P}(\mathcal{P}(\mathcal{F}))$ be two sets of products and let $A, B \in \mathcal{F}$ be two features. We define the following operators:

•
$$[[nil]] = \emptyset$$

•
$$[[\nu]] = \{\emptyset\}$$

•
$$[[\lambda; \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
=
$$[[\Lambda; \cdot]](\mathcal{P}) = \{\{\Lambda\} \cup p | p \in \mathcal{P}\}$$

•
$$[[\Lambda; \cdot]](\mathcal{P}) = \{\emptyset\} \cup \{\{\Lambda\} \cup p | p \in \mathcal{P}\}$$

•
$$[[\cdot \lor \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
=
$$[[\cdot \lor \cdot]](\mathcal{P}, Q) = \mathcal{P} \cup Q$$

•
$$[[\cdot \land \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \times \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
=
$$[[\cdot \land \cdot]](\mathcal{P}, Q) = \{p \cup q | p \in \mathcal{P}, q \in Q\}$$

•
$$[[\Lambda \Rightarrow B \text{ in } \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\Lambda \Rightarrow B \text{ in } \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\Lambda \Rightarrow B \text{ in } \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\Lambda \Rightarrow B \text{ in } \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\Lambda \Rightarrow B \text{ in } \cdot]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\Lambda \Rightarrow A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\Lambda \Rightarrow A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\cdot \Rightarrow A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\cdot \setminus A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\cdot \setminus A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\cdot \setminus A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\cdot \setminus A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }
= [[\cdot \setminus A]] : \mathcal{P}(\mathcal{P}(\mathcal{F})) \mapsto \mathcal{P}(\mathcal{P}(\mathcal{F})) \text{ as }$$$$$$$$

With these operators over sets of products, we can define the denotational semantics of any SPLA expression, which is defined inductively in the usual way.

Definition 8. The denotational semantics of SPLA is the function $[\![\ \cdot \]\!] : SPLA \to \mathcal{P}(\mathcal{P}(\mathcal{F}))$ inductively defined as follows: for any *n*-ary operator

$$\begin{split} & \mathsf{op} \in \{ \texttt{nil}, \mathbf{\nu}, \mathsf{A}; \cdot, \overline{\mathsf{A}}; \cdot, \cdot \lor \cdot, \land \cdot, \mathsf{A} \Rightarrow \mathsf{B} \texttt{ in} \cdot, \mathsf{A} \Rightarrow \mathsf{B} \texttt{ in} \cdot, \cdot \Rightarrow \mathsf{A}, \cdot \setminus \mathsf{A} \}^2 \texttt{:} \\ & [\![\mathsf{op}(P_1, \ldots, P_n)]\!] = [\![\mathsf{op}]\!] ([\![P_1]\!], \ldots, [\![P_n]\!]) \end{split}$$

Example 6. In order to illustrate the denotational semantics, we apply it to the examples presented in Fig. 4. The results are presented in Fig. 9.

The rest of this section is devoted to proving that the set of products computed by the operational semantics coincides with the one computed by the denotational semantics. In order to do it, first we need some auxiliary results that relate the operational semantics with the denotational operators from Definition 7.

The first result deals with the termination of a trace. The products of an SPL are computed from the bottom up. That means that the first product *computed* by the denotational semantics is the product with no features. Let us note that $\{\emptyset\} = \text{prod}(\mathbf{\nu}) = \llbracket \mathbf{\nu} \rrbracket$, but $\emptyset = \text{prod}(\text{nil}) = \llbracket \text{nil} \rrbracket$.

Lemma 2. Let us consider $P \in \text{SPLA}$, if $P \xrightarrow{\checkmark} \text{nil}$ then $\emptyset \in \llbracket P \rrbracket$.

$ \begin{bmatrix} \checkmark \\ [\overline{B}; \checkmark] \\ [A; \overline{B}; \checkmark] \end{bmatrix} $	$ \begin{aligned} \mathbf{a} \\ = \{ \varnothing \} \\ = [\![\overline{\mathbf{B}}; \cdot]\!] ([\![\checkmark]\!]) = [\![\overline{\mathbf{B}}; \cdot]\!] (\{ \varnothing \}) = \{ \varnothing, \{ \mathbf{B} \} \} \\ = [\![\mathbf{A}; \cdot]\!] ([\![\overline{\mathbf{B}}; \checkmark]\!]) = [\![\mathbf{A}; \cdot]\!] (\{ \varnothing \} \cup \{ \mathbf{B} \}) = \\ \{ \{ \mathbf{A} \}, \{ \mathbf{A}, \mathbf{B} \} \} \end{aligned} $
[[B; √]] [[A; B; √]]	$ \begin{split} \mathbf{b} \\ = \llbracket \mathbf{B}; \cdot \rrbracket (\llbracket \checkmark \rrbracket) = \llbracket \mathbf{B}; \cdot \rrbracket (\{ \varnothing \}) = \{ \{ \mathbf{B} \} \} \\ = \llbracket \mathbf{A}; \cdot \rrbracket (\llbracket \mathbf{B}; \checkmark \rrbracket) = \llbracket \mathbf{A}; \cdot \rrbracket (\{ \mathbf{B} \}) = \{ \{ \mathbf{A}, \mathbf{B} \} \} \end{split} $
$\begin{bmatrix} B; \checkmark \end{bmatrix} \\ \begin{bmatrix} C; \checkmark \end{bmatrix} \\ \begin{bmatrix} B; \checkmark \lor C; \checkmark \end{bmatrix} \\ \begin{bmatrix} A; (B; \checkmark \lor C; \checkmark) \end{bmatrix}$	$c \\ = \{\{B\}\} \\ = \{\{C\}\} \\ = [\![\lor \lor \cdot]]([\![B; \checkmark]\!], [\![C; \checkmark]\!]) = \\ [\![B; \checkmark]\!] \cup [\![C; \checkmark]\!] = \{\{B\}, \{C\}\} \\ = [\![A; \cdot]]([\![B; \checkmark \lor C; \checkmark]\!]) = \\ [\![A; \cdot]](\{\{B\}, \{C\}\}) = \{\{A, B\}, \{A, C\}\} $
$ \begin{split} & \llbracket B; \checkmark \land C; \checkmark \rrbracket \\ & \llbracket \cdot \land \cdot \rrbracket (\{\{B\}\}, \{\{C\}\} \\ & \llbracket A; (B; \checkmark \land C; \checkmark)\rrbracket \end{split} $	$\begin{array}{l} \mathbf{d} \\ = \llbracket \cdot \land \cdot \rrbracket (\llbracket B; \checkmark \rrbracket, \llbracket C; \checkmark \rrbracket) = \\ \}) = \{ \{B, C\} \} \\ = \llbracket A; \cdot \rrbracket (\{ \{B, C\} \}) = \\ \{ \{A, B, C\} \} \end{array}$
$\llbracket \overline{\mathbf{B}}; \checkmark \land \mathbf{C}; \checkmark) \rrbracket$ $\llbracket \mathbf{A}; (\overline{\mathbf{B}}; \checkmark \land \mathbf{C}; \checkmark) \rrbracket$	$e \\ = \llbracket \cdot \land \cdot \rrbracket (\{ \emptyset, \{ B \} \}, \{ \{ C \} \}) = \\ \{ \{ C \}, \{ B, C \} \} \\ = \llbracket A; \cdot \rrbracket (\llbracket \overline{B}; \checkmark \land C; \checkmark) \rrbracket) = \\ \llbracket A; \cdot \rrbracket (\{ \{ C \}, \{ B, C \} \}) = \\ \{ \{ A, C \}, \{ A, B, C \} \}$
$\llbracket \overline{B}; \checkmark \land \overline{C}; \checkmark \rrbracket$	$ \begin{split} \mathbf{f} \\ = & \llbracket \cdot \land \cdot \rrbracket (\llbracket \overline{\mathbb{B}}; \checkmark \rrbracket, \llbracket \overline{\mathbb{C}}; \checkmark \rrbracket) = \\ & \llbracket \cdot \land \cdot \rrbracket (\lbrace \varnothing, \lbrace B \rbrace\}, \lbrace \varnothing, \lbrace C \rbrace\}) = \\ & \lbrace \varnothing, \lbrace B \rbrace, \lbrace C \rbrace, \lbrace B, C \rbrace \\ & \llbracket \cdot \varPi \\ & \llbracket \cdot \varPi \rbrace \\ & \llbracket \cdot \varPi \\ & \llbracket \cdot \varPi \\ & \llbracket \cdot \varPi \\ & \rrbracket \\ & \llbracket \cdot \varPi \\ & \rrbracket \\ & \llbracket \cdot \varPi \\ & \rrbracket \\ & \rrbracket \\ & \llbracket \cdot \varPi \\ & \rrbracket \\ & \rrbracket \\ & \rrbracket \\ & \llbracket \\ & \rrbracket $
$\begin{bmatrix} A; (\overline{B}; \checkmark \land \overline{C}; \checkmark) \end{bmatrix}$ $\begin{bmatrix} B \neq C \text{ in } A; \\ (\overline{B}; \checkmark \land \overline{C}; \checkmark) \end{bmatrix}$	$ = \llbracket A; \cdot \rrbracket (\llbracket \overline{B}; \checkmark \land \overline{C}; \checkmark \rrbracket) = \\ \llbracket A; \cdot \rrbracket (\{ \varnothing, \{B\}, \{C\}, \{B, C\}\}) = \\ \{\{A\}, \{A, B\}, \{A, C\}, \{A, B, C\}\} = \\ \llbracket B \not\Rightarrow C \text{ in } \cdot \rrbracket \begin{pmatrix} \{\{A\}, \{A, B\}, \\ \{A, C\}, \{A, B, C\}\} \end{pmatrix} = \\ \{\{A\}, \{A, C\}, \{A, B\}\} \end{pmatrix} $
$\left[\begin{array}{c} B \Rightarrow C \text{ in } A; \\ (\overline{B}; \checkmark \land \overline{C}; \checkmark) \end{array} \right]$	$ \begin{array}{c} \mathbf{g} \\ = [\![\mathbb{B} \Rightarrow \mathbb{C} \text{ in } \cdot]\!] \left(\begin{array}{c} \{\{\mathbb{A}\}, \{\mathbb{A}, \mathbb{B}\}, \\ \{\mathbb{A}, \mathbb{C}\}, \{\mathbb{A}, \mathbb{B}, \mathbb{C}\}\} \end{array} \right) \\ \{\{\mathbb{A}\}, \{\mathbb{A}, \mathbb{B}, \mathbb{C}\}, \{\mathbb{A}, \mathbb{C}\}\} \end{array} \right) $

Fig. 9. Application of the denotational semantic rules.

Next we will present a lemma for each operator of the syntax. Each of these lemmas indicates that the corresponding semantic operator is well defined in Definition 7. These results will be needed in the inductive case of Theorem 1.

Lemma 3. Let $P, P' \in SPLA$, and $A, B \in \mathcal{F}$, then

- 1. prod(A;P) = [[A;](prod(P))
- 2. $\operatorname{prod}(\overline{A}; P) = \llbracket A; \rrbracket(\operatorname{prod}(P))$
- 3. $\operatorname{prod}(P \lor P') = \llbracket \lor \rrbracket(\operatorname{prod}(P), \operatorname{prod}(P')).$
- 4. $\operatorname{prod}(P \wedge P') = \llbracket \land \rrbracket(\operatorname{prod}(P), \operatorname{prod}(P')).$
- 5. $\operatorname{prod}(P \Rightarrow A) = \llbracket \cdot \Rightarrow A \rrbracket(\operatorname{prod}(P)).$
- 6. $\operatorname{prod}(P \setminus A) = \llbracket \cdot \setminus A \rrbracket(\operatorname{prod}(P))$.
- 7. $\operatorname{prod}(A \Rightarrow \operatorname{Bin} P) = [A \Rightarrow \operatorname{Bin} \cdot](\operatorname{prod}(P)).$
- 8. $\operatorname{prod}(A \Rightarrow \operatorname{Bin} P) = [A \Rightarrow \operatorname{Bin} \cdot](\operatorname{prod}(P)).$

¹ If *X* is a set, $\mathcal{P}(X)$ denotes the power set of *X*.

² nil and ν are 0-ary operators; $A; \cdot, \overline{A}; \cdot A \Rightarrow Bin \cdot , A \Rightarrow Bin \cdot , \cdot \Rightarrow A, \cdot \setminus A$ are 1-ary operators; $\cdot \vee \cdot$ and $\cdot \wedge \cdot$ are 2-ary operators.

Now we have the result we were looking for: The denotational semantics and the operational semantics are equivalent.

Theorem 1. Let $P \in \text{SPLA}$, then $\text{prod}(P) = \llbracket P \rrbracket$.

An immediate result from the previous theorem is that the equivalence relation \equiv is a congruence.

Corollary 1. The equivalence relation \equiv is a congruence: For any *n*-ary operator op, and $P_1, \ldots, P_n, Q_1, \ldots, Q_n \in \text{SPLA}$ such that $P_1 \equiv Q_1, \ldots, P_n \equiv Q_n$, we have

$$\operatorname{op}(P_1,\ldots,P_n) \equiv \operatorname{op}(Q_1,\ldots,Q_n)$$

5.1. Correctness of the translation

The translation procedure in Section 3.2 does not impose an order among the restrictions. This means that one diagram can be translated in different syntactical terms in SPLA. In this section we are going to prove that all that different terms are equivalent indeed. In the translation, we first codify the require constraints from the FODA diagram. When all the *require* constraints are codified, then the *exclude* constraints are codified. First, let us prove the property that indicates the order in which the *require* constraints are chosen. Let us recall that when making the translation from FODA to SPLA we consider the closure of the *require* constraints.

First, let us prove the property that indicates the order in which the *require* constraints are chosen. Let us recall that when making the translation from FODA to SPLA we are going to consider the closure of the *require* constraints.

Definition 9. Let $P \in \text{SPLA}$ be term, we say that is *closed with respect the require constraints* if has the following form:

$$\mathtt{A}_1 \Rightarrow \mathtt{B}_1 \text{ in } \mathtt{A}_2 \Rightarrow \mathtt{B}_2 \text{ in } \cdots \mathtt{A}_n \Rightarrow \mathtt{B}_n \text{ in } Q$$

where Q has no restrictions and the set of restrictions is closed by transitivity, that is, if there are features $A, B, C \in \mathcal{F}$ and $1 \leq ij \leq n$ such that $A = A_i$, $B = B_i = A_j$ and $C = B_j$ then there is $1 \leq k \leq n$ such that $A = A_k$ and $C = B_k$.

Proposition 3. Let $P \in SPLA$ be a closed term with respect to the require constraints. Let us consider $Q' \in SPLA$ by reordering the require constraints in *P*. Under these conditions $P \equiv Q$.

We also have to prove that the order in which the *exclude* constraints are chosen is irrelevant. This is because two *exclude* constrains in FODA are always interchangeable.

Proposition 4. Let $P \in \text{SPLA}$ be a term and $A, B, C, D \in \mathcal{F}$, then $A \neq \text{BinC} \neq \text{Din}P \equiv C \neq D$ in $A \neq \text{Bin}P$.

5.2. Full abstraction

Finally in this Section, we show that the model is fully abstract: given any set *S* of products, there is a SPLA term whose semantics is exactly the set *S*. Moreover, this term can be constructed by using a subset of the grammar: nil, \checkmark , the prefix operator, and the choice operator.

Definition 10. Let $P \in SPLA$, we say that it is a *basic term* if it can be generated by the following grammar

 $P ::= \mathbf{i} | \text{nil} | A; P | P \lor Q$

We denote the set of basic terms as SPLA_b.

The result that we are looking for is the following.

Theorem 2. Let \mathcal{F} be a finite set of features and let $A \in \mathcal{P}(\mathcal{P}(\mathcal{F}))$, there exists $P \in SPLA_b$ such that prod(P) = A.

This result indicates also an important practical consequence. Let us imagine any other operator that could be added to the syntax of SPLA. This result indicates that this operator can be *derived* from the operators in SPLA_b. Even the operators in SPLA can be rewritten in terms of the operators in SPLA_b. In fact, in the next section we show how we can remove the non-basic operators from any SPLA term.

This result establishes also an interesting theoretical consequence. Since the denotational semantics is fully abstract, it is isomorphic to the initial model with respect to the equivalence relation. That is, the set of products with the operators of the denotational semantics is isomorphic to the algebra of terms: SPLA/=.

6. Axiomatic semantics

In this section we give an axiomatic semantics for SPLA, presenting *sound* and *complete* axioms for the language. As usual, *soundness* means that the equalities deduced from the axiom system are indeed correct: $P = {}_{EQ}$ implies $P \equiv Q$. The completeness means that all the identities can be deduced from the axiom system, that is $P \equiv Q$ implies $P = {}_{EQ}$.

Definition 11. Let P, $Q \in SPLA$. We say that we deduce the equivalence of P and Q if $P = _{EQ}$ can be deduced from the set of equations in Figs. 10–13.

To prove the soundness it is enough to show that the operators are congruent (Theorem 1) and that each axiom is correct.

Proposition 5. Let $A, B \in \mathcal{F}$ be two features, and P and Q be terms of SPLA. The equations in Figs. 10–13 are correct.

To prove the completeness we need the concept of *normal forms*. In order to define the normal forms we prove that some operators are *derived* from the *basic* operators. These basic operators are the base ones (nil and \nvdash), the prefix operator (A; P), and the *choose-one* operator ($P \lor Q$). The following example shows how some operators can be removed.

Example 7. Let us consider the following SPL P = A; $\checkmark \land B$; \checkmark . It is easy to compute its successful traces which are {AB,BA}, so $\text{prod}(P) = \{[AB]\}$. This SPL has the same products as A; B; \checkmark .

$[\textbf{EXCL1}] \ \texttt{A} \not\Rightarrow \texttt{B} \ \texttt{in} \ (\texttt{C}; P) \mathop{=_E} \texttt{C}; (\texttt{A} \not\Rightarrow \texttt{B} \ \texttt{in} \ P)$
$[\textbf{EXCL2}] \ \texttt{A} \not\Rightarrow \texttt{B} \ \texttt{in} \ (\texttt{A}; P) \mathop{=_E} \texttt{A}; (P \backslash \texttt{B})$
$[\mathbf{EXCL3}] \ \mathtt{A} \not\Rightarrow \mathtt{B} \ \mathtt{in} \ (\mathtt{B}; P) =_E \mathtt{B}; (P \backslash \mathtt{A})$
$[\mathbf{EXCL4}] \ \mathbf{A} \not\Rightarrow \mathbf{B} \ \mathbf{in} \ P \lor Q =_E \ (\mathbf{A} \not\Rightarrow \mathbf{B} \ \mathbf{in} \ P) \lor (\mathbf{A} \not\Rightarrow \mathbf{B} \ \mathbf{in} \ Q)$
$[\mathbf{EXCL5}]$ A $ eq$ B in $\checkmark=_E\checkmark$
$[\mathbf{EXCL6}] \ \texttt{A} \not\Rightarrow \texttt{B} \ \texttt{in} \ \texttt{nil} =_E \texttt{nil}$
$[\textbf{FORB1}] \ (\textbf{A}; P) \backslash \textbf{A} =_E \texttt{nil}$
$[\mathbf{FORB2}] \ (B; P) \backslash A =_E B; (P \backslash A)$
$[\mathbf{FORB3}] \checkmark \backslash \mathtt{A} =_E \checkmark$
$[\mathbf{FORB4}]$ nil/A $=_E$ nil
$[\mathbf{FORB5}] \ (P \lor Q) \backslash \mathbf{A} =_E (P \backslash \mathbf{A}) \lor (Q \backslash \mathbf{A})$

Fig. 11. Equations to remove exclusion, and forbid operators.

[CON1]	$(\mathbf{A};P)\wedge Q=_E\mathbf{A};(P\wedge Q)$
[CON2]	$P \wedge Q =_E Q \wedge P.$
[CON3]	$P \land (Q \lor R) =_E (P \land Q) \lor (P \land R).$
[CON4]	$P \wedge \mathtt{nil} =_E \mathtt{nil}$
[CON5]	$P \land \checkmark =_E P$

Fig. 12. Axioms to remove the conjunction operator.

[PRE1] A; B; $P =_E$ B; A; P. [PRE2] \overline{A} ; $P =_E (A; P) \lor \checkmark$. [PRE3] $(A; P) \lor (A; Q) =_E A$; $(P \lor Q)$ [PRE4] A; nil =_E nil [PRE5] A; A; $P =_E A$; P [CH01] $P \lor Q =_E Q \lor P$. [CH02] $(P \lor Q) \lor R =_E P \lor (Q \lor R)$. [CH03] $P \lor$ nil =_E P. [CH04] $P \lor P = P$.

Fig. 13. Axioms for basic operators and optional features.

Indeed, by applying the indicated axioms we have the following deduction

A; $(\checkmark \land B; \checkmark) = E$ [CON1] A; $(\checkmark \land B; \checkmark) = E$ [CON2] A; (B; $\checkmark \land \checkmark) = E$ [CON1] A; B; $(\checkmark \land \checkmark) = E$ [CON5] A; B; $(\checkmark \land \checkmark) = E$ [CON5]

The set of axioms in Figs. 10–12, plus the axiom [**PRE 2**] in Fig. 13 allow the non-basic operators (Definition 10) to be removed

from any $P \in SPLA$. The idea is to prove that $Q \in SPLA_b$ exists such that $P \equiv Q$.

Let us suppose that we have a term $P \in SPLA$ that contains a non-basic operator. Then we can find $Q \in SPLA$ where either, the non-basic operator has disappeared or it is deeper in the syntactic tree of Q. Then iterating this process we can make all non-basic operators disappear. Then we have the theorem we are looking for.

Theorem 3. Let $P \in SPLA$, there exists $Q \in SPLA_b$ such that $P = {}_EQ$.

Since we know how to remove the non-basic operators of an SPLA term, we focus on proving the completeness restricted to basic terms. To do so, we define are our *normal forms*, and then we prove that any basic term can be transformed to a normal form by using the basic axioms in Fig. 13.

In order to give the formal definitions of normal forms we provide auxiliary definitions. First we assume that there is an order relation $\leq \subseteq \mathcal{F} \times \mathcal{F}$ that must be isomorphic to the natural numbers in case \mathcal{F} is infinite. Next, we need the *vocabulary* of a basic SPLA term, that is the set of features appearing in the expression.

Definition 12. Let *P*, $Q \in \text{SPLA}_b$ be two basic SPLA terms. We define the *vocabulary* as the function $\text{voc} : \text{SPLA}_b \to \mathcal{P}(\mathcal{F})$ defined inductively as:

- voc(nil) = voc(𝒴) = ∅
- $\operatorname{voc}(A;P) = \{A\} \cup \operatorname{voc}(P)$
- $\operatorname{voc}(P \lor Q) = \operatorname{voc}(P) \cup \operatorname{voc}(Q)$

Before giving the definition of normal forms, we define a simpler case that is the case of pre-normal forms.

Definition 13. A basic SPLA term $P \in SPLA_b$ is in *pre-normal form*, written $P \in SPLA_{pre}$, iff it has one of the following forms.

1. nil, **//, or**

2. There exists $n > 0, \{A_1, \ldots, A_n\} \subseteq \mathcal{F}$, and there exist $P_1, \ldots, P_n \in \text{SPLA}_{pre}$ with $P_i \neq \text{nil for } 1 \leq i \leq n \text{ and } \{A_1, \ldots, A_n\} \cap \text{voc}(P_i) = \emptyset \text{ for } 1 \leq j \leq n \text{ and either}$

$$P = (A_1; P_1) \vee \cdots (A_n; P_n)$$

or

$$P = (A_1; P_1) \vee \cdots (A_n; P_n) \vee \checkmark$$

In this case we say that the features $\{A_1, \ldots, A_n\}$ are at the top level of *P*.

Next we present an auxiliary lemma that will be used in Proposition 8. This lemma establishes that if a feature appears in the vocabulary of a pre-normal form then it appears in at least one product of the pre-normal form. Let us note that this result is not true³ in ordinary terms because of the restrictions that might appear in the terms.

Lemma 4. Let $P \in \text{SPLA}_{pre}$, then

 $\mathsf{voc}(P) = \{ \mathtt{A} | \mathtt{A} \in p, \ p \in \mathtt{prod}(P) \}$

The next lemma establishes that if a feature A appears in a prenormal form P, the normal form can be transformed into another equivalent normal form Q so that A is at the top level of the syntax tree of Q.

³ The vocabulary of an ordinary term has not been formally defined. Definition 12 could easily be extended to the set of features appearing in the syntax of a term.

Lemma 5. Let $P \in SPLA_{pre}$ and let $A \in voc(P)$. Then there is $Q \in SPLA_{pre}$ such that $P \equiv Q$ and $A \in \{A_1, ..., A_n\}$ according to condition 2 of Definition 13 applied to Q.

The next proposition establishes the first result we need to prove the completeness: Any term can be transformed into an equivalent pre-normal form. The result is restricted to basic terms but, because of Theorem 3, it can be extended to any ordinary term.

Proposition 6. Let $P \in SPLA_b$, there exists a pre-normal form $Q \in SPLA_{pre}$ such that $P = {}_{E}Q$.

The problem with pre-normal forms is that there are syntactically different expressions that are equivalent, as the following example shows.

Example 8. Let us consider the following SPLA_b expressions:

$$P = (A; C; \checkmark) \lor B; \checkmark$$
$$Q = (C; A; \checkmark) \lor B; \checkmark$$

Both expressions are in pre-normal form and both are equivalent.

The way to obtain a unique normal form for any $SPLA_b$ expression is to use the above mentioned order among features. Let us assume A < B < C; in this case we say that *P* is in normal form while *Q* is not.

Normal forms are a particular case of pre-normal forms that overcome this problem. They make use of the order required between features.

Definition 14. Let us consider $P \in \text{SPLA}_{pre}$. We will say that P is a normal form, written $P \in \text{SPLA}_{nf}$ iff $P = \text{nil}, P = \checkmark$ or if the sets $\{A_1, \ldots, A_n\}$ and $\{P_1, \ldots, P_n\}$ in Definition 13.2 satisfy:

• $A_i < A_j$ for $1 \leq i < j \leq n$.

• $A_i < B$ for any $B \in voc(P_j)$ for $1 \le i \le j \le n$.

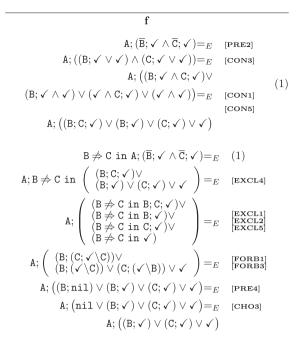
Example 9. Figs. 14–16 show the normal forms corresponding to the examples in Fig. 4 assuming that A < B < C. The examples **a**, **b**, and **c** are not included due to the fact that they are already normal forms.

Now we have our first result. Any expression can be transformed into a normal form.

Proposition 7. Let $P \in \text{SPLA}_{pre}$. Then there exists a normal form $Q \in \text{SPLA}_{nf}$ such that $P = {}_{EQ}$.

The next result shows that two normal forms that are semantically equivalent, are also identical at the lexical level.

d				
$\mathtt{A}; (\mathtt{B}; \checkmark \land \mathtt{C}; \checkmark) =_E \text{[con1]}$				
$A; B; (\checkmark \land C; \checkmark) =_E$ [CON2], [CON5]				
A; B; C; ✓				
е				
$\mathtt{A}; (\overline{\mathtt{B}}; \checkmark \land \mathtt{C}; \checkmark) =_E \texttt{[CON2]}, \texttt{[CON1]}$				
$A; C; (\checkmark \land \overline{B}; \checkmark) =_E$ [CON2], [CON5]				
$A;C;\overline{B};\checkmark=_E [\mathbf{PRE2}]$				
$A; C; (B; \checkmark \lor \checkmark) =_E \text{[Pre3]}$				
$\mathtt{A}; (\mathtt{C}; \mathtt{B}; \checkmark \lor \mathtt{C}; \checkmark) =_E \text{[pre1]}$				
$\mathtt{A}; (\mathtt{B}; \mathtt{C}; \checkmark \lor \mathtt{C}; \checkmark)$				





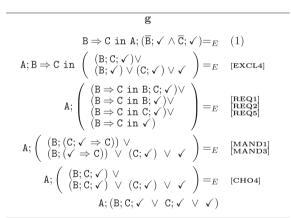


Fig. 16. Transformation to normal form 3/3.

Proposition 8. Let P, $Q \in SPLA_{nf}$. If they are semantically equivalent, $P \equiv Q$, then they are syntactically identical P = Q.

Finally, we can prove the main result of this section: The deductive system is sound and complete.

Theorem 4. Let us consider $P,Q \in SPLA$. Then $P \equiv Q$ if and only if $P = {}_{E}Q$.

7. Checking satisfiability

In this section we present a mechanism to check the *satisfiability* of a syntactical term, that is, whether there is a product satisfying all restrictions imposed by the term.

Definition 15. Let $P \in \text{SPLA}$. We say that P is satisfiable iff $\text{prod}(P) \neq \emptyset$.

Checking the satisfiability of any $P \in SPLA$ could be done by computing the products *P*, by using the rules defining the denotational semantics. Once we have this set we could check whether it is empty or not. However, computing all products may be not feasible. So, in this section we present an alternative that uses a SAT-solver. From any $P \in SPLA$ we build a propositional formula $\phi(P)$ such that P is satisfiable if and only if there exists a valuation v such that $v \models \phi(P)$. In building such a formula we keep track of the order in which features are produced. Any feature A is associated with a set of boolean variables: A_k for $k \in \mathbb{N}$. The integer associated with the feature is used to keep track of the order in which it has been produced. Therefore, our boolean variables will have the form A_k where $A \in \mathcal{F}$ and $k \in \mathbb{N}$.

Before describing how compute the formula associated to a syntactical term, we need some auxiliary definitions. The maxin function is especially important because it is used to compute the next index available for a feature in a given formula.

Definition 16. Let φ be a propositional formula, we denote *the set* of boolean variables appearing in φ by vars(φ).

Let $A \in \mathcal{F}$ and φ be a propositional formula. We define the function that returns the *maximum index of* A *in the formula* φ as follows:

$$\mathsf{maxin}(\mathbb{A}, \varphi) = \begin{cases} k & \exists l \in \mathbb{N} : \mathbb{A}_l \in \mathsf{vars}(\varphi), \\ & k = \max\{l | A_l \in \mathsf{vars}(\varphi)\} \\ -1 & \mathsf{otherwise} \end{cases}$$

Finally, if l < 0, A_l will denote the symbol \perp .

Lemma 7 is necessary in order to prove the main result of this section. But in order to prove Lemma 7 we need to *complete* the computed formulas in the presence of the choice operator. It is convenient that the function maxin is same in both members of a choice operator, this is achieved by the completing of a formula.

Definition 17. Given φ_1 and φ_2 , we define the *completion of* φ_1 *up to* φ_2 , written $\varphi_1^{\Rightarrow \varphi_2}$, as follows:

$$\begin{split} \varphi_1 \wedge & \bigwedge_{\mathbf{A} \in \mathcal{F},} (\neg \mathbf{A}_l \to \neg \mathbf{A}_{l-1}) \wedge \cdots \wedge (\neg \mathbf{A}_{k+1} \to \neg \mathbf{A}_k) \\ l = \max(\mathbf{A}, \varphi_2), \\ k = \max(\mathbf{A}, \varphi_1), \\ \mathbf{0} \leqslant k < l \end{split}$$

The first consequence of the previous definition is that $\varphi_1^{\Rightarrow \varphi_2}$ is stronger than φ_1 : if $v \models \varphi_1^{\Rightarrow \varphi_2}$ then $v \models \varphi_1$. let us note that in the previous definition, the new variables do not belong to φ_1 . So any valuation v such that $v \models \varphi_1$ can be extended to a new valuation v' in such a way that it only modifies the value of the new variables and $v' \models \varphi_1^{\Rightarrow \varphi_2}$. It is also important to note that the number of variables does not increase due to this completion. This is because the variables that we introduce in $\varphi_1^{\Rightarrow \varphi_2}$ are already in φ_2 . These properties are expressed in the following lemma.

Lemma 6. Let φ_1 and φ_2 be two propositional formulas.

- 1. Let *v* be a valuation such that $v \models \varphi_1^{\Rightarrow \varphi_2}$, then $v \models \varphi_1$.
- 2. Let v be a valuation such that $v \models \varphi_1$. Then there is a valuation v' such that $v' \models \varphi_1^{\Rightarrow \varphi_2}$ and $v'(A_l) = v(A_l)$ for any feature A and $0 \le l \le maxin(A, \varphi_1)$.
- 3. Let A be a feature such that there is $k \in \mathbb{N}$ satisfying $A_k \in \operatorname{vars}(\varphi_1^{\Rightarrow \varphi_2})$ but $A_k \notin \operatorname{vars}(\varphi_1)$, then $A_k \in \operatorname{vars}(\varphi_2)$. So $\max(A, \varphi_1^{\Rightarrow \varphi_2}) = \max(A, \varphi_2)$

Definition 18. Let $P \in SPLA$, we define its associated propositional formula, written $\phi(P)$, as follows:

$$\phi(\texttt{nil}) = \bot$$
$$\phi(\checkmark) = \top$$

 $\phi(A; P) = A_{l+1} \land \phi(P)$ where $l = \max(0, \max(A, \phi(P)))$

$$egin{aligned} &\phi(\overline{A};P) = op \ &\phi(P \lor Q) = \phi(P)^{\Rightarrow \phi(Q)} \lor \phi(Q)^{\Rightarrow \phi(P)} \ &\phi(P \land Q) = \phi(P) \land \phi(Q) \end{aligned}$$

$$\begin{split} \phi(\textbf{A} \Rightarrow \textbf{B} \text{ in } P) &= (\neg \textbf{A}_{l+1} \rightarrow \neg \textbf{A}_l) \land (\neg \textbf{B}_{m+1} \rightarrow \neg \textbf{B}_m) \\ \land (\textbf{A}_{l+1} \rightarrow \textbf{B}_{m+1}) \land \phi(P) \\ & \text{ where } l = \text{maxin}(\textbf{A}, \phi(P)) \text{ and } m = \text{maxin}(\textbf{B}, \phi(P)) \end{split}$$

$$\begin{split} \phi(\mathbb{A} \not\Rightarrow \mathbb{B} \text{ in } P) &= (\neg \mathbb{A}_{l+1} \to \neg \mathbb{A}_l) \land (\neg \mathbb{B}_{m+1} \to \neg \mathbb{B}_m) \\ \land (\neg \mathbb{A}_{l+1} \lor \neg \mathbb{B}_{m+1}) \land \phi(P) \\ \text{ where } l &= \max(\mathbb{A}, \phi(P)) \text{ and } m = \max(\mathbb{B}, \phi(P)) \end{split}$$

$$\phi(P \Rightarrow A) = A_{l+1} \land \phi(P)$$
 where $l = \max(0, \max(A, \phi(P)))$
 $\phi(P \land A) = \neg A_l \land \phi(P)$ that where $l = \max(A, \phi(P))$

Before giving the result that will help us to check the satisfiability of any $P \in SPLA$, we need a preliminary property of $\phi(P)$.

Lemma 7. Let $P \in \text{SPLA}, A \in \mathcal{F}, v$ be a valuation such that $v \models \phi(P)$ and $l \in \mathbb{N}$ such that $v(A_l) = 0$ and $A_l \in \text{vars}(\phi(P))$, then $v(A_k) = 0$ for $k \leq l$.

We want to prove that there exists $p \in SPLA$ if and only if the formula $\phi(P)$ is satisfiable. But there are problems in the presence of restrictions (requires, excludes, mandatory or forbid) inside a conjunction operator. So the result is restricted to syntactical terms that do not have restrictions inside the conjunction operator. This is not a major drawback since the restrictions can be considered to be external to the other operators.

Definition 19. Let $P \in SPLA$, we say that is *a safe SPL* if there are no restrictions inside a conjunction operator (\land).

The main result of this section is Theorem 5. This Theorem relates the satisfiability of an SPL *P* and the satisfiability of its associated formula $\phi(P)$. This theorem is a direct consequence of Propositions 9 and 10. Proposition 9 is the *left to right implication* of Theorem 5. It is proven by structural induction on *P*, and an extra condition is needed to prove the result.

Proposition 9. Let $P \in \text{SPLA}$ be a safe SPL. If $p \in \text{prod}(P)$ then there is a valuation v such that $v \models \phi(P)$, and $A \in p$ iff $k \ge 0$ and $v(A_k) = 1$ where $k = \max(A, \phi(P))$

Proposition 10 is the *right to left implication* of Theorem 5. It is also proven by structural induction on *P*. As in the previous case, an extra condition is needed to prove the result.

Proposition 10. Let $P \in \text{SPLA}$ be a safe SPL. If there is a valuation v such that $v \models \phi(P)$ then there is a product $p \in \text{prod}(P)$ such that $A \notin p$ for any feature A satisfying $v(A_l) = 0$ for all $0 \leq \text{lmaxin}(A, \phi(P))$.

So, finally we have the result we require.

Theorem 5. Let $P \in \text{SPLA}$ be a safe SPL, then $p \in \text{prod}(P)$ iff there is a valuation v such that $v \models \phi(P)$.

Finally, in Section 9 we present the results of a tool implementing this result. This tool uses a SAT-solver. Let us note that $\phi(P)$ is not a CNF formula. So, in order to check its satisfiability with a SATsolver, $\phi(P)$ has to be converted into CNF.

8. Study of vss system

In this section we consider the example of a model of a Video Streaming Software. The FODA Diagram of our Video Streaming Software is presented in Fig. 17. This system incorporates the following features: VSS, TBR, VCC, 720Kbps, 256Kbps, H.264 and MPEG.2. The *initial feature*⁴ for this SPL is VSS (Video Streaming Software). Any product of this SPL will need this first feature. Let us note that each feature has a unique name the definition of which appears in the domain terminology dictionary of *Video Streaming Software*.

Next we present the features, and we indicate their intuitive meanings and relationships between them. The initial first feature is VSS. Its related features are: TBR (the Transmission Bit-Rate) and VCC (the Video Codec) features. There is a mandatory relationship between them (it is represented by using an arc). This relationship establishes that this set of features is included in all implementations of the software where the VSS is included. There are additional mandatory relationships in the diagram. For instance, the feature 720Kbps has a relationship with its parent. There are other features in this system that are included as optional. For instance, the feature 256Kbps is optional, meaning that this feature may be (or may not be) included in the final products of the SPL. Let us note that the inclusion of this feature depends on other relationships that represent constraints. Finally, let us consider the features H.264 and MPEG.2. The choose-one operators relates to them. This means that at least one of this features will be presented in the final product of the SPL. In this system, there is a set of rules that denotes some restrictions. For instance, when the feature MPEG.2 is included, the feature 720Kbps is also included. Furthermore, when the feature H.264 is selected, the feature 256Kbps must not appear. Let us consider the constraint that relates MPEG.2 with the speed 720Kbps. It is a require constraint, that is, if the MPEG.2 feature is selected, then the speed of the TBR must be 720Kbps. Let us note that this constraint is not necessary since 720Kbps is mandatory, we have included it to show that sometimes FODA allows the inclusion of repetitive (or useless) information. There is another constraint that focuses on the features H.264 and 256Kbps and it is an exclusion constraint meaning that if the feature H.264 appears then the final product cannot contain 256Kbps. Finally, a valid product of this model is a set of features that fulfills all the constraints in the diagram. For instance, let us consider the following products: pr1 consists of VSS, TBR, 720Kbps, VCC, and H.264, pr₂ consists of VSS, TBR, 720Kbps, VCC, and MPEG.2 and pr₃ consists of VSS, TBR, 720Kbps, 256Kbps. VCC, and H.264. In this case, pr1 and pr2 are valid products of this model, but pr_3 is not a valid product of this model.

In order to present the formal analysis, first we focus on the *translation process*. Formally, the complete translation of this model into our algebra is P_1 :

 $\begin{array}{l} P_1:= \texttt{MPEG.2} \ \Rightarrow \texttt{720Kbps in} \\ \texttt{H.264} \ \Rightarrow \texttt{256Kbps in} \ P_2 \end{array}$

where

 $\begin{array}{l} P_2 := \texttt{VSS}; (P_{21} \land P_{22}) \\ P_{21} := \texttt{TBR}; (\texttt{720Kbps}; \checkmark \land \overline{\texttt{256Kbps}}; \checkmark) \\ P_{22} := \texttt{VCC}; (\texttt{H.264}; \checkmark \lor \texttt{MPEG.2}; \checkmark) \end{array}$

We depict the labeled transition system associated with the term P_1 in Fig. 18. In this figure we have skipped the subtrees (1) and (2) because we did not obtain new products from those subtrees. From the tree depicted we obtain the following traces:

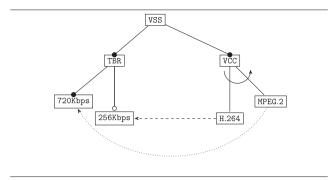


Fig. 17. Video Streaming Software - FODA representation.

- VSS TBR VCC 720Kbps H.264,
- VSS TBR VCC 720Kbps MPEG.2 720Kbps,
- VSS TBR VCC 720Kbps MPEG.2 256Kbps 720Kbps,
- VSS TBR VCC MPEG.2 720Kbps, and
- VSS TBR VCC MPEG.2 720Kbps 256Kbps.

From those traces we obtain the products:

- {VSS, TBR, VCC, 720Kbps, H.264},
- {VSS, TBR, VCC, 720Kbps, MPEG.2}, and
- {VSS, TBR, VCC, 720Kbps, MPEG.2, 256Kbps}

The denotational semantics for P_1 is presented in Fig. 19. Let us note that the set of products is not modified after applying the last semantic operator [MPEG.2 \Rightarrow 720Kbps in \cdot]]. This means that the *require* restriction is not necessary in this case. As expected (see Theorem 1), the set of products obtained by applying the denotational semantics coincides with the set of products computed with the operational semantics: $prod(P_1) = [P_1]$.

Finally, we present the *deduction process* induced by the axioms in Fig. 20. We can observe how the initial expression P_1 is transformed until we obtain a pre-normal form (Fig. 20, Eq. (5)):

VSS; TBR; VCC; 720Kbps; (H.264 ;

∨ MPEG.2;720Kbps;(256Kbps; ∠ ∨ ∠))

This term is not in normal form for two reasons. First we have not established an order among the features, instead we can assume the following ordering: VSS < TBR < VCC < 720Kbps < H.264 < MPEG.2 < 256Kbps. Secondly, the repetition of feature 720Kbps in this term is not allowed in normal forms. We obtain the normal from by applying the rules in Eq. (6), and the resulting term is:

```
VSS; TBR; VCC; 720Kbps; (H.264 ;  ✓ ∨ MPEG.2 ; (256Kbps;  ✓ ∨ ))
```

This is a normal form for the above ordering.

9. The Spla Tool

In this section we present the SPLA Tool, called AT. The core of this tool is implemented in JAVA. The tool can be downloaded from http://simba.fdi.ucm.es/at and its license is GPL v3.⁵

The tool has modules that will be presented later. In order to validate them, we generate SPLs using the JAVA BeTTy Feature Model Generator⁶ [40]. In BeTTy, the models are generated by setting several input parameters. We have used the following values: The percentage of constraints was 30%, the probability of a feature

⁴ Sometime called the hard-system of the SPL.

⁵ More details in http://www.gnu.org/copyleft/gpl.html.

⁶ Can be downloaded from http://www.isa.us.es/betty/betty-online.

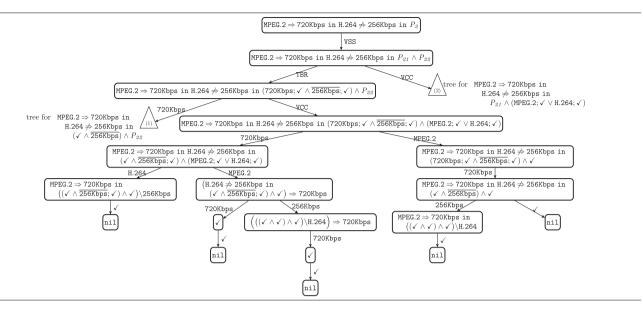


Fig. 18. Labeled transitions system of the video streaming software.

$ \begin{bmatrix} \checkmark \end{bmatrix} &= \{ \varnothing \} \\ \begin{bmatrix} \text{H.264} ; \checkmark \end{bmatrix} &= \{ \{ \text{H.264} \} \cup \varnothing \} = \{ \{ \text{H.264} \} \} \\ \begin{bmatrix} \text{MPEG.2} ; \checkmark \end{bmatrix} &= \{ \{ \text{MPEG.2} \} \} \\ \begin{bmatrix} \text{T20Kbps} ; \checkmark \end{bmatrix} &= \{ \forall \text{T20Kbps} \} \cup \varnothing \} = \{ \varnothing, \{ \text{256Kbps} \} \} $
$ \begin{split} \llbracket P_{22} \rrbracket &= \llbracket \text{VCC; } (\text{H.264; } \checkmark \lor \text{MPEG.2; } \checkmark) \rrbracket = \llbracket \text{VCC; } \cdot \rrbracket (\llbracket \lor \rrbracket (\{ \text{H.264} \}, \{ \{ \text{MPEG.2} \})) = \\ \llbracket \text{VCC; } \cdot \rrbracket (\llbracket \text{H.264; } \checkmark \rrbracket \cup \llbracket \text{MPEG.2; } \checkmark \rrbracket) = \llbracket \text{VCC; } \cdot \rrbracket (\{ \{ \text{H.264} \}, \{ \text{MPEG.2} \}) = \\ \{ \{ \text{VCC; } \text{H.264}, \{ \text{VCC; } \text{MPEG.2} \} \} \\ \llbracket P_{21} \rrbracket &= \llbracket \text{TBR; } (\text{720Kbps; } \checkmark \land \text{726Kbps; } \checkmark) \rrbracket = \\ \llbracket \text{TBR; } \cdot \rrbracket (\llbracket \land \rrbracket (\{ \{ \text{720Kbps} \}, \{ \text{720Kbps} \}, \{ \text{720Kbps} \} \}) = \\ \llbracket \text{TBR; } \cdot \rrbracket (\{ \{ \text{T20Kbps} \}, \{ \text{TBR, 720Kbps} \} \}) = \\ \{ \{ \text{TBR; 720Kbps} \}, \{ \text{TBR, 720Kbps} \} \} \end{split} $
$ \begin{split} \llbracket P_{21} \wedge P_{22} \rrbracket &= \llbracket \wedge \rrbracket \left(\begin{array}{c} \{ \{ \text{VCC}, \text{H.264} \}, \{ \text{VCC}, \text{MPEG.2} \} \}, \{ \{ \text{TBR}, \text{720Kbps} \}, \\ \{ \text{TBR}, \text{720Kbps}, 256 \text{Kbps} \} \end{array} \right) = \\ \left\{ \begin{array}{c} \{ \text{VCC}, \text{TBR}, \text{H.264}, \text{720Kbps} \}, \{ \text{VCC}, \text{TBR}, \text{H.264}, \text{720Kbps}, 256 \text{Kbps} \}, \\ \{ \text{VCC}, \text{TBR}, \text{MPEG.2}, \text{720Kbps} \}, \{ \text{VCC}, \text{TBR}, \text{M264}, \text{720Kbps}, 256 \text{Kbps} \}, \\ \{ \text{VCC}, \text{TBR}, \text{MPEG.2}, \text{720Kbps} \}, \{ \text{VCC}, \text{TBR}, \text{M264}, \text{720Kbps}, 256 \text{Kbps} \}, \\ \{ \text{VSS}; (P_{21} \wedge P_{22}) \rrbracket = \llbracket \text{VSS}; \ (P_{21} \wedge P_{22}) = \\ \left\{ \begin{array}{c} \{ \text{VSS}, \text{VCC}, \text{TBR}, \text{H.264}, \text{720Kbps} \}, \{ \text{VSS}, \text{VCC}, \text{TBR}, \text{H.264}, \text{720Kbps}, 256 \text{Kbps} \}, \\ \{ \text{VSS}, \text{VCC}, \text{TBR}, \text{H.264}, \text{720Kbps} \}, \{ \text{VSS}, \text{VCC}, \text{TBR}, \text{M266}, \text{720Kbps}, 256 \text{Kbps} \}, \\ \{ \text{VSS}, \text{VCC}, \text{TBR}, \text{MPEG.2}, \text{720Kbps} \}, \{ \text{VSS}, \text{VCC}, \text{TBR}, \text{M266}, \text{720Kbps}, 256 \text{Kbps} \}, \\ \} \end{split} \right\} \end{split}$
$ \begin{bmatrix} \text{H}.264 \neq 256\text{Kbps in } P_2 \end{bmatrix} = \begin{bmatrix} \text{H}.264 \neq 256\text{Kbps in } \cdot] (\llbracket P_2 \rrbracket) = \\ \left\{ \begin{array}{l} \text{VSS, VCC, TBR, H.264, 720\text{Kbps}}, \\ \text{VSS, VCC, TBR, MPEG.2, 720\text{Kbps}}, \\ \text{VSS, VCC, TBR, MPEG.2, 720\text{Kbps}}, \\ \text{VSS, VCC, TBR, MPEG.2, 720\text{Kbps}} \end{array} \right\} \\ = \begin{bmatrix} \text{MPEG.2 } \neq 720\text{Kbps in } P_2 \end{bmatrix} = \\ \left\{ \begin{array}{l} \text{VSS, VCC, TBR, MPEG.2, 720\text{Kbps}}, \\ \text{VSS, VCC, TBR, MPEG.2, 720\text{Kbps}}, \\ \text{VSS, VCC, TBR, H.264, 720\text{Kbps}}, \\ \text{VSS, VCC, TBR, MPEG.2, 720\text{Kbps}}, \\ \end{array} \right\} $

Fig. 19. Denotational semantics of video streaming software.

being mandatory was 25%, the probability of a feature being in a *choose-one* relation was 50%.

9.1. Satisfiability module

This module computes the satisfiability of an SPL by using the results in Section 9.1. So it computes the formula associated with an SPL. Then, by using a SAT-solver, we check the satisfiability of the formula. Since the module has been developed in JAVA, we have used the SAT4j.

The experiments were run with a number of different features ranging from 1000 to 13,500. Fig. 21 shows the time needed to compute the satisfiability of the SPLs with respect to the number of features in the model.

9.2. Denotational semantic module

This module computes the products of an SPLA term according to the semantics presented in this paper.

The experiments were run with a number of different features ranging from 50 to 300. The results are in Fig. 22. There are three columns of values in this table: The first one contains the number of features, the second one contains the time required to complete the experiment, and the last one contain the number of products needed to give an answer. This number is a lower bound of the total number of features of the model with the property that if it is 0 then the model is not satisfiable. A dash in the last column indicates that we did not obtain any answer after a 15 min timeout.
$$\begin{split} \text{TBR;} & (720\text{Kbps};\checkmark\land\overline{256\text{Kbps}};\checkmark) =_E \quad [\text{CON1}] \\ \text{TBR;} & 720\text{Kbps}; (\checkmark\land\overline{256\text{Kbps}};\checkmark) =_E \quad [\text{CON5}] \quad [\text{CON2}] \end{split}$$

TBR; 720Kbps; $\overline{256Kbps}$; $\sqrt{=_E}$ [**PRE2**]

TBR; 720Kbps; (256Kbps; $\checkmark \lor \checkmark$)	
$P_{2} = \text{VSS}; (P_{21} \land P_{22}) =_{E} (2)$ $\text{VSS}; (\text{TBR}; 720\text{Kbps}; (256\text{Kbps}; \checkmark \lor \checkmark)) \land (\text{VCC}; (\text{H.264}; \checkmark \lor \text{MPEG.2}; \checkmark)) =_{E} [\text{CON1}]$ $\text{VSS}; \text{TBR}; \text{VCC}; 720\text{Kbps}; ((256\text{Kbps}; \checkmark \lor \checkmark) \land (\text{H.264}; \checkmark \lor \text{MPEG.2}; \checkmark)) =_{E} [\text{CON3}]$ $\text{VSS}; \text{TBR}; \text{VCC}; 720\text{Kbps}; (((256\text{Kbps}; \checkmark \lor \checkmark) \land ((256\text{Kbps}; \checkmark \lor \checkmark) \land \text{MPEG.2}; \checkmark)) =_{E} [\text{CON1}]$ $\text{VSS}; \text{TBR}; \text{VCC}; 720\text{Kbps}; ((1256\text{Kbps}; \checkmark \lor \checkmark) \land (1266\text{Kbps}; \checkmark \lor \checkmark) \land \text{MPEG.2}; \checkmark) =_{E} [\text{CON1}]$ $\text{VSS}; \text{TBR}; \text{VCC}; 720\text{Kbps}; (1266\text{Kbps}; \checkmark \lor \checkmark) \land \lor) \lor \text{MPEG.2}; ((256\text{Kbps}; \checkmark \lor \checkmark) \land \checkmark)) =_{E} [\text{CON5}]$ $\text{VSS}; \text{TBR}; \text{VCC}; 720\text{Kbps}; (\text{H.264}; (256\text{Kbps}; \checkmark \lor \checkmark) \lor \text{MPEG.2}; (256\text{Kbps}; \checkmark \lor \checkmark))$	(8
H.264 \Rightarrow 256Kbps in $P_2 =_E$ (3)	
$\texttt{H.264} \neq \texttt{256Kbps in } \Big(\texttt{VSS}; \texttt{TBR}; \texttt{VCC}; \texttt{720Kbps}; \big(\texttt{H.264}; (\texttt{256Kbps}; \checkmark \lor \checkmark) \lor \texttt{MPEG.2}; (\texttt{256Kbps}; \checkmark \lor \checkmark) \big) \Big) =_E [\texttt{EXCL1}]$	
VSS; TBR; VCC; 720Kbps; H.264 \neq 256Kbps in $((H.264; (256Kbps; \checkmark \lor \checkmark) \lor MPEG.2; (256Kbps; \checkmark \lor \checkmark))) =_E [EXCL4]$	
$VSS; TBR; VCC; 720Kbps; ((H.264 \neq 256Kbps in H.264; (256Kbps; \checkmark \lor \checkmark))) \lor$	
$(\text{H.264} \neq \text{256Kbps in MPEG.2; (256Kbps; \checkmark \lor \checkmark))} =_E [\text{EXCL2}]$	
VSS; TBR; VCC; 720Kbps; $((H.264; ((256Kbps; \checkmark \lor \checkmark))256Kbps))\lor$	
$(\text{H.264} \neq 256\text{Kbps in MPEG.2}; (256\text{Kbps}; \checkmark \lor \checkmark)) =_E [\text{EXCL1}]$	
$\texttt{VSS}; \texttt{TBR}; \texttt{VCC}; \texttt{720Kbps}; ((\texttt{H.264}; ((\texttt{256Kbps}; \checkmark \lor \checkmark) \land \texttt{256Kbps})) \lor$	(
$(\texttt{MPEG.2};\texttt{H.264} \not\Rightarrow \texttt{256Kbps in } (\texttt{256Kbps};\checkmark\lor\checkmark)) =_E [\texttt{FORB1}] [\texttt{FORB3}] [\texttt{FORB5}]$	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; \big(\texttt{H.264};\checkmark\vee(\texttt{MPEG.2};\texttt{H.264} \not\Rightarrow \texttt{256Kbps} \texttt{ in } (\texttt{256Kbps};\checkmark\vee\checkmark))\big) =_E [\texttt{EXCL3}]$	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; \big((\texttt{H.264};((\texttt{256Kbps};\checkmark\lor\checkmark)\backslash\texttt{256Kbps})) \lor$	
$(\texttt{MPEG.2}; \texttt{H.264} \Rightarrow \texttt{256Kbps} \texttt{ in } (\texttt{256Kbps}; \checkmark \lor \checkmark)) =_E [\texttt{FORB1}] [\texttt{FORB3}] [\texttt{FORB5}]$	
$\texttt{VSS}; \texttt{TBR}; \texttt{VCC}; \texttt{720Kbps}; (\texttt{H.264}; \checkmark \lor (\texttt{MPEG.2}; \texttt{H.264} \not\Rightarrow \texttt{256Kbps} \texttt{ in } (\texttt{256Kbps}; \checkmark \lor \checkmark))) =_E [\texttt{EXCL3}] [\texttt{EXCL4}] [\texttt{EXCL5}]$	
$VSS; TBR; VCC; 720Kbps; (H.264; \checkmark \lor (MPEG.2; ((256Kbps; \checkmark) \backslash H.264 \lor \checkmark))) =_E [\mathbf{FORB2}] [\mathbf{FORB3}] [\mathbf{FORB5}]$	
$\texttt{VSS}; \texttt{TBR}; \texttt{VCC}; \texttt{720Kbps}; (\texttt{H.264}; \checkmark \lor (\texttt{MPEG.2}; (\texttt{256Kbps}; \checkmark \lor \checkmark)))$	
$P_{I} = \text{MPEG.2} \Rightarrow 720\text{Kbps in H.264} \Rightarrow 256\text{Kbps in } P_{2} =_{E} (4)$ MPEG.2 \Rightarrow 720Kbps in VSS; TBR; VCC; 720Kbps; (H.264; $\checkmark \lor \text{MPEG.2}; (256\text{Kbps}; \checkmark \lor \checkmark)) [\text{REQ1}]$ VSS; TBR; VCC; 720Kbps; MPEG.2 \Rightarrow 720Kbps in (H.264; $\checkmark \lor \text{MPEG.2}; (256\text{Kbps}; \checkmark \lor \checkmark)) [\text{REQ4}]$	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps};\left((\texttt{MPEG.2} \Rightarrow \texttt{720Kbps in H.264}; \checkmark) \lor (\texttt{MPEG.2} \Rightarrow \texttt{720Kbps in MPEG.2}; (\texttt{256Kbps}; \checkmark \lor \checkmark))\right) [\texttt{REQ1}]$	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; \big(\texttt{H.264}; (\texttt{MPEG.2} \Rightarrow \texttt{720Kbps} \texttt{ in } \checkmark) \lor \big(\texttt{MPEG.2} \Rightarrow \texttt{720Kbps} \texttt{ in } \big(\texttt{256Kbps}; \checkmark \lor \checkmark)\big)\big) [\texttt{REQ5}]$	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; \left(\texttt{H.264};\checkmark\lor(\texttt{MPEG.2}\Rightarrow\texttt{720Kbps}\texttt{ in }\texttt{MPEG.2};)\right) [\texttt{REQ2}]$	(
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; \big(\texttt{H.264};\checkmark\lor\texttt{MPEG.2}; \big((\texttt{256Kbps};\checkmark\lor\checkmark)\Rightarrow\texttt{720Kbps})\big) [\texttt{MAND5}]$	
$\texttt{VSS}; \texttt{TBR}; \texttt{VCC}; \texttt{720Kbps}; (\texttt{H.264}; \checkmark \lor \texttt{MPEG.2}; ((\texttt{256Kbps}; \checkmark \Rightarrow \texttt{720Kbps}) \lor (\checkmark \Rightarrow \texttt{720Kbps}))) [\texttt{MAND5}]$	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; (\texttt{H.264};\checkmark\lor\texttt{MPEG.2}; ((\texttt{256Kbps};\checkmark\Rightarrow\texttt{720Kbps})\lor(\checkmark\Rightarrow\texttt{720Kbps}))) [\texttt{MAND2}] [\texttt{MAND3}]$	
VSS; TBR; VCC; 720Kbps; (H.264; $\checkmark \lor$ MPEG.2; 720Kbps; (256Kbps; $\checkmark \lor \checkmark)$)= $_E$ [PRE2]	
$\texttt{VSS};\texttt{TBR};\texttt{VCC};\texttt{720Kbps}; \left(\texttt{H.264}; \checkmark \lor \texttt{MPEG.2};\texttt{720Kbps}; \left(\texttt{256Kbps}; \checkmark \lor \checkmark \right)\right)$	
VSS; TBR; VCC; 720Kbps; (H.264; $\checkmark \lor$ MPEG.2; 720Kbps; (256Kbps; $\checkmark \lor \lor)) =_E$ [PRE1]	
VSS; TBR; VCC; 720Kbps; (H.264; $\checkmark \lor$ 720Kbps; MPEG.2; (256Kbps; $\checkmark \lor \checkmark$))= $_E$ [PRE3]	
$VSS; TBR; VCC; 720Kbps; (H.264; \checkmark \lor 720Kbps; MPEG.2; (256Kbps; \checkmark \lor \checkmark)) =_E [PRE3]$ $VSS; TBR; VCC; (720Kbps; H.264; \checkmark \lor 720Kbps; 720Kbps; MPEG.2; (256Kbps; \checkmark \lor \checkmark)) =_E [PRE5]$	
VSS; TBR; VCC; 720Kbps; (H.264; $\checkmark \lor$ 720Kbps; MPEG.2; (256Kbps; $\checkmark \lor \checkmark$))= $_E$ [PRE3]	

Fig. 20. Deduction rules applied to the video streaming software.

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(2)

Features	Time (ms.)	Features	Time (ms.)
01000	67	07500	529
01500	71	08000	556
02000	105	08500	682
02500	140	09000	428
03000	164	09500	639
03500	153	10000	620
04000	193	10500	396
04500	410	11000	513
05000	331	11500	575
05500	339	12000	552
06000	441	12500	413
06500	289	13000	513
07000	369	13500	503

Fig. 21. Satisfiability benchmark.

eatures	Time (ms.)	Products
50	6	48
60	25	108
70	15	104
80	8	31
90	38	0
100	55	1404
110	13	12
120	2139	1
130	511	24802
140	7	6
150	786126	1312848
160	136	5670
170	42	398
180	744	6384
190	1390	7232
200	960000	-
210	97770	800544
220	263	51
230	47	8
240	65	29
250	191	5920
260	205	7296
270	250	4301
280	960000	-
290	65	3
300	960000	-

Fig. 22. Denotational benchmark.

10. Conclusions and future work

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In this paper we presented SPLA as a general framework to represent SPLs, showing how it can be used to provide FODA diagrams with a formal semantics. This semantics is fully abstract, and so any other operator that could be used to define an SPL can be represented in terms of SPLA. This suggest that the formalism is quite general and we anticipate that it can be used to express any other formalism like those mentioned in [41]. In particular, Theorem 2 showed that we work with a fully abstract model. We defined three different semantics for SPLA: an operational semantics, a denotational semantics and an axiomatic semantics. We proved that the semantics are equivalent. Besides defining the formal framework, we have developed a tool to show the applicability of our formal techniques. The tool is available at http://simba.fdi.ucm.es/at, and it has been developed under the GPL v3 license.

Since SPLA is based on process algebras, we plan to take advantage of the previous work in this field. In particular, we plan to study alternative semantics. For instance, in our current semantics the products are only sets of features. That is, the order in which the features are computed is not important. There are situations where this is no longer true. That could be the case if we consider the cost associated with the production of a product. For instance producing feature A and then B could have cost $3.00 \in$, but producing B and then A might cost $2.00 \in$. We also plan to include non-functional aspects to SPLA such as probabilities, that could be useful in building a user model [42], timed characteristics and, as we have mentioned, costs.

Another interesting feature that has been deeply studied in the field of process algebras is the probabilistic information. We think that this is an interesting feature that should studied in the world of Software Product Lines. In this way, we can deduce the probability of the products. This can be applied, for example, in software testing, so that we can add more resources to test the products with higher probabilities.

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Appendix A. Proofs of the results

Proof Lemma 1. The proof is done by applying induction on the derivation of $P \stackrel{\checkmark}{\rightarrow} Q$. We have to observe that in all rules producing transitions like $P \stackrel{\checkmark}{\rightarrow} Q$ we see that Q is \checkmark . \Box

Proof Lemma 2. We prove this by induction on the length of the deduction of $P \stackrel{\checkmark}{\rightarrow} \text{nil}$, let us consider that this length is *n*.

- n = 0 In this case we have two cases: $P = \nu$ and $P = \overline{A}$; P, in both cases $\emptyset \in [\![P]\!]$.
- n > 1 In this case we have to analyze the rules that yields P → nil. These rules are [cho1],[cho2], [con3],[req3], [excl4], and [forb2].
- **Rules** [cho1] and [cho2]. These rules are symmetric so we can concentrate in one of them, for instance [**cho1**]. In this case $P = P_1 \lor P_2$ and $P_1 \stackrel{\checkmark}{\rightarrow} \text{nil}$. By induction $\emptyset \in \llbracket P_1 \rrbracket$, then, by Definition 7, $\emptyset \in \llbracket P \rrbracket$. **Rule** [con3]. In this case $P = P_1 \land P_2$, $P_1 \xrightarrow{\checkmark}$ nil, and $P_2 \xrightarrow{\checkmark} \text{nil.}$ By induction $\emptyset \in \llbracket P_1 \rrbracket$ and $\emptyset \in \llbracket P_2 \rrbracket$. We have the result by Definition 7. **Rule** [req3]. In this case $P = A \Rightarrow BinP_1$ and $P_1 \xrightarrow{\checkmark} \text{nil. By induction } \emptyset \in \llbracket P_1 \rrbracket$. Since $A \notin \emptyset$, by Definition 7, $\emptyset \in \llbracket P \rrbracket$. **Rule** [excl4]. In this case $P = A \Rightarrow Bin P_1$ and $P_1 \xrightarrow{\checkmark} \text{nil. By induction } \emptyset \in \llbracket P_1 \rrbracket$. Since $A \notin \emptyset$ and $B \notin \emptyset$, by Definition 7, $\emptyset \in \llbracket P \rrbracket$. **Rule [req3]**. In this case $P = P_1 \setminus A$ and $P_1 \xrightarrow{\checkmark} nil$. By induction $\emptyset \in \llbracket P_1 \rrbracket$. Since $A \notin \emptyset$, by Definition 7, $\emptyset \in \llbracket P \rrbracket$. \Box

Proof (*Lemma 3.1*). Since the only rule of the operational semantics that can be applied to A;P is [**feat**], we obtain $tr(A;P) = \{A \cdot s | s \in tr(P)\}$.

Thus $prod(A;P) = \{A \cup p | p \in prod(P)\}\)$, that is the definition of [A;](prod(P)) (see Definition 7). \Box

Proof (*Lemma 3.2*). The rules that can be applied to \overline{A} ; *P* are **[ofeat1]** and **[ofeat2]**. So we obtain $tr(A; P) = \{ \checkmark \} \cup \{A \cdot s | s \in tr(P) \}$. Therefore $prod(\overline{A}; P) = \{ \emptyset \} \cup \{A \cup p | p \in prod(P) \}$, that is the definition of $[\overline{A};](prod(P))$ (see Definition 7). \Box

Proof (*Lemma* 3.3). From rules [**cho**1] and [**cho**2] we obtain $tr(P \lor P') = tr(P) \cup tr(P')$. Thus

$$\texttt{prod}(P \lor P') = \texttt{prod}(P) \cup \texttt{prod}(P') = \llbracket \lor \rrbracket(\texttt{prod}(P),\texttt{prod}(P')) \quad \Box$$

Proof (*Lemma 3.4*). First, let us consider $[s] \in prod(P \land P')$, we will show that $[s] \in [\land](prod(P), prod(P'))$ by induction on the length of *s*.

- $s = \epsilon$. In this case $[s] = \emptyset$ and $P \land P' \xrightarrow{\sim} \text{nil}$. We can only apply rule [con3] to obtain this transition. So $P \xrightarrow{\sim} \text{nil}$ and $P' \xrightarrow{\sim} \text{nil}$. Thus $\emptyset \in \text{prod}(P)$ and $\emptyset \in \text{prod}(P')$. Then $[s] = \emptyset \in [\land](\text{prod}(P), \text{prod}(P'))$ by Definition 7.
- $s = A \cdot s'$. In this case $P \wedge P' \stackrel{A}{\to} P''$. We can apply rules [**con**1] or [**con**2] to obtain this transition. [**con**2] is symmetric to [**con**1], so we can concentrate on [**con**1]. P_1 exists so that $P \stackrel{A}{\to} P_1, P'' = P_1 \wedge P'$ and $[s'] \in prod(P_1 \wedge P')$. By induction $[s'] \in [\![\wedge]\!](prod(P_1), prod(P'))$, so there are products $p_1 - e prod(P_1)$ and $p_2 \in prod(P')$ such that $[s'] = p_1 \cup p_2$. Let us consider the trace $s_1 \in tr(P_1)$ such that $[s_1] = p_1$. Then

$$[\mathbf{S}] = \{\mathbf{A}\} \cup [\mathbf{S}'] = \{\mathbf{A}\} \cup p_1 \cup p_2 = (\{\mathbf{A}\} \cup [\mathbf{S}_1]) \cup p_2$$

Finally we obtain the result by Definition 7 since $A \cdot s_1 \in tr(P)$ and $p_2 \in prod(P')$.

Now let us consider $p \in [\land] (\operatorname{prod}(P), \operatorname{prod}(P'))$. By Definition 7 there are $p_1 \in \operatorname{prod}(P)$ and $p_2 \in \operatorname{prod}(P')$ such that $p = p_1 \cup p_2$. So there are successful traces $s_1 \in \operatorname{tr}(P)$ and $s_2 \in \operatorname{tr}(P')$ such that $[s_1] = p_1$ and $[s_2] = p_2$. We make the proof by induction on the sum of the lengths of s_1 and s_2 .

- $len(s_1) + len(s_2) = 0$. So $s_1 = \epsilon, s_2 = \epsilon$ and $p = \emptyset$. In this case we have the transitions $P \stackrel{\checkmark}{\rightarrow} nil$ and $P' \stackrel{\backsim}{\rightarrow} nil$. By applying rule [**con5**], we have the transition $P \land P' \stackrel{\backsim}{\rightarrow} nil$. Thus $\emptyset \in prod(P \land P')$.
- *len*(*s*₁) + *len*(*s*₂) > 0. Let us suppose that *s*₁ = A ⋅ *s'*₁, (the case $s_2 = A \cdot s_2$ is symmetric). Then, there is a transition $P \xrightarrow{A} P_1$ such that s'_1 is a successful trace of P_1 . By Definition 7, $[s'_1] \cup [s_2] \in [\land]]$ (prod(P_1), prod(P')). By induction $[s'_1] \cup [s_2] \in prod(P_1 \land P')$. By applying rule [**con**1], we obtain the transition $P \land P' \xrightarrow{A} P_1 \land P'$. Then $\{A\} \cup [s'_1] \cup [s_2] \in prod(P \land P')$. We have the desired result since $[s_1] = \{A\} \cup [s'_1]$. \Box

Proof (Lemma 3.5). First we are going to prove

 $\texttt{prod}(P \Rightarrow \texttt{A}) \subseteq \llbracket \cdot \Rightarrow \texttt{A} \rrbracket(\texttt{prod}(P))$

So let us consider $p \in \operatorname{prod}(P \Rightarrow A)$. A successful trace $s \in \operatorname{tr}(P \Rightarrow A)$ exists so that [s] = p. We will show that $p \in [\![\cdot \Rightarrow A]\!](\operatorname{prod}(P))$ by induction on the length of *s*. Because of Rule [mand1], $s \neq \epsilon$. Thus the base case is when s = A ($p = \{A\}$). Also because of Rule [mand1] we have the transition $P \stackrel{\checkmark}{\rightarrow} \operatorname{nil}$, so $\emptyset \in \operatorname{prod}(P)$. Therefore, by Definition 7 $p = \{A\} \cup \emptyset \in [\![\cdot \Rightarrow A]\!](\operatorname{prod}(P))$.

Now let us consider $s = \mathbb{B} \cdot s'$. If $\mathbb{A} = \mathbb{B}$, we have the transition $P \Rightarrow \mathbb{A} \xrightarrow{A} P_1$ and $s' \in \operatorname{tr}(P_1)$. This transition can only be deduced by Rule [**mand2**]. Therefore $P \xrightarrow{A} P_1$ and $s \in \operatorname{tr}(P)$. To obtain the result it is only necessary to take into account that $\mathbb{A} \in [s]$, so $[s] = \{\mathbb{A}\} \cup [s]$. If $\mathbb{A} \neq \mathbb{B}$, we have the transition $P \Rightarrow \mathbb{A} \xrightarrow{B} P_1 \Rightarrow \mathbb{A}$ and $s' \in \operatorname{tr}(P_1 \Rightarrow \mathbb{A})$. This transition can only be deduced by Rule [**mand3**]. Therefore $P \xrightarrow{B} P_1$. By induction we have $[s'] \in [\cdots \Rightarrow \mathbb{A}](\operatorname{prod}(P_1))$. Thus, by Definition 7, there is $p \in \operatorname{prod}(P_1)$ such that $p_1 \cup \{\mathbb{A}\} = [s']$. There must exist $s_1 \in \operatorname{tr}(P)$, such that $[s_1] = p_1$. Now we can observe that $\mathbb{B} \cdot s_1 \in \operatorname{tr}(P)$, thus $\{\mathbb{B}\} \cup p_1 - \in \operatorname{prod}(P)$. Thus

$$p = [\mathtt{S}] = \{\mathtt{B}\} \cup [\mathtt{S}'] = \{\mathtt{B}\} \cup p_1 \cup \{\mathtt{A}\} \in \llbracket \cdot \Rightarrow \mathtt{A}
rbracket(\mathtt{prod}(P))$$

Now we are going to prove

$$[\cdot \Rightarrow A] (\operatorname{prod}(P)) \subseteq \operatorname{prod}(P \Rightarrow A)$$

So let us consider $p \in [\![\cdot \Rightarrow A]\!](prod(P))$. Then, by Definition 7, there is $p' \in prod(P)$ such that $p = p' \cup \{A\}$. There exists $s \in tr(P)$ such that [s] = p'. We will show that $p \in prod(P \Rightarrow A)$ by induction on the length of s.

 $s = \epsilon$. In this case we have $P \stackrel{\checkmark}{\rightarrow} \text{nil}$ and $p' = \emptyset$. Then by applying Rules [mand1] and [tick], we obtain

$$P \Rightarrow A \xrightarrow{A} \checkmark \checkmark \rightarrowtail nil$$

Then $p = p' \cup \{A\} = \{A\} \in prod(P \Rightarrow A)$. $s = A \cdot s'$ In this case we have the transition $P \xrightarrow{A} P'$ and $s' \in tr(P')$. Then, by Rule [mand2], we obtain $P \Rightarrow A \xrightarrow{A} P'$. Therefore $s \in tr(P \Rightarrow A)$. To get the result it is enough to take into account that $A \in [s]$, so $p = p' \cup \{A\} = [s] \cup \{A\} = [s]$.

 $s = \mathbb{B} \cdot s'$ with $\mathbb{A} \neq \mathbb{B}$.

In this case we have $P \xrightarrow{\mathbb{B}} P'$ and $s' \in tr(P')$. By Definition 7,

$$\{A\} \cup [S'] \in \llbracket \cdot \Rightarrow A \rrbracket (\operatorname{prod}(P'))$$

By induction we get

$$\{A\} \cup [S'] \in \operatorname{prod}(P' \Rightarrow A)$$

There is a trace $s'' \in tr(P' \Rightarrow A)$ such $[s''] = \{A\} \cup [s']$. By applying Rule [mand3], $P \Rightarrow A \xrightarrow{B} P' \Rightarrow A$. Finally, since $B \cdot s'' \in tr(P \Rightarrow A)$, we obtain

$$\begin{split} p &= p' \cup \{\mathtt{A}\} = [s] \cup \{\mathtt{A}\} = \{\mathtt{B}\} \cup [s'] \cup \{\mathtt{A}\} = \{\mathtt{B}\} \cup [s''] \\ &\in \texttt{prod}(P \Rightarrow \mathtt{A}) \quad \Box \end{split}$$

Proof (Lemma 3.6). First we are going to prove

 $\operatorname{prod}(P \setminus A) \subseteq \llbracket \cdot \setminus A \rrbracket(\operatorname{prod}(P))$

So let us consider $p \in prod(P \setminus A)$. A successful trace $s \in tr(P \setminus A)$ exists such that [s] = p. We will show that $p \in [\cdot \setminus A](prod(P))$ by induction on the length of s.

- $s = \epsilon$ In this case we have $p = \emptyset$ and $P \setminus A \xrightarrow{\sim} nil$. The only applicable rule is Rule [**forb2**], thus $P \xrightarrow{\sim} nil$. By Lemma 2, $\emptyset \in prod(P)$. By Definition 7 $p = \emptyset \in prod(P \setminus A)$.
- $s = \mathbb{B} \cdot s'$ In this case $P \setminus A \xrightarrow{B} P' \setminus A$ and $s' \in tr(P' \setminus A)$. Since *s* is successful, *s'* is also successful. Thus $[s'] \in prod(P' \setminus A)$. By induction $[s'] \in [\![\cdot \setminus A]\!](P')$, therefore $[s'] \in prod(P' \setminus A)$. By induction $[s'] \in [\![\cdot \setminus A]\!](P')$, therefore $[s'] \in prod(P')$ and $A \notin [s']$. Since the only applicable rule to deduce $P \setminus A \xrightarrow{B} P' \setminus A$ is Rule [**forb1**], we obtain $\mathbb{B} \cdot s' \in tr(P)$ and $\mathbb{B} \neq A$. Therefore $[s] \in tr(P)$ and $A \notin [s]$, so, by Definition 7, $p = [s] \in [\![\cdot \setminus A]\!](prod(P))$.

Now we are going to prove

$$[\cdot \setminus A](\operatorname{prod}(P)) \subseteq \operatorname{prod}(P \setminus A)$$

So let us consider $p \in [\cdot \setminus A](prod(P))$. By Definition 7 $p \in prod(P)$ and $A \notin p$. Therefore there exists a successful trace $s \in tr(P)$ such that [s] = p. Since $A \notin [s]$, we obtain that s is a successful trace of $P \setminus A$. Therefore $p = [s] \in prod(P \setminus A)$. \Box

Proof (Lemma 3.7). First we are going to prove

 $\operatorname{prod}(A \Rightarrow B \text{ in } P) \subseteq \llbracket A \Rightarrow B \text{ in } \cdot \rrbracket(\operatorname{prod}(P))$

So let us consider $p \in \text{prod}(A \Rightarrow \text{Bin } P)$. There exists a successful trace $s \in \text{tr}(A \Rightarrow \text{Bin} P)$ such that [s] = p. We will show that $p \in [A \Rightarrow \text{Bin} \cdot](\text{prod}(P))$ by induction on the length of *s*.

 $s = \epsilon$. In this case we have the transition

 $\operatorname{prod}(A \Rightarrow B \text{ in } P) \xrightarrow{\nu} \operatorname{nil}$

The only applicable rule is Rule [**req3**]. So $P \xrightarrow{\checkmark} \text{nil}$ and therefore $\emptyset \in \text{prod}(P)$. So by Definition 7, $\emptyset \in [A \Rightarrow \text{Bin} \cdot](\text{prod}(P))$.

- $s = C \cdot s'$ with $C \neq A$. In this case we have the transition $A \Rightarrow B \text{ in } P \stackrel{\circ}{\rightarrow} P'$. If $A \neq C$, by Rule [**req1**], we obtain $P' = A \Rightarrow B \text{ in } P_1$ with $P \stackrel{\circ}{\rightarrow} P_1$, and $s' \in tr(A \Rightarrow B \text{ in } P_1)$ By induction, we obtain $[s'] \in [A \Rightarrow B \text{ in } \cdot](\text{prod}(P_1))$. By Definition 7, we have two cases
- $A \notin [s']$. In this case $[s'] \in prod(P_1)$, therefore $\{C\} \cup [s'] \in prod(P)$ and, since $C \neq A$ by Definition 7, $\{C\} \cup [s'] \in [A \Rightarrow Bin \cdot]$ (prod(P))
- $A \in [s']$. In this case, by Definition 7, there is a trace $s'' \in tr(P_1)$ such that $[s'] = [s''] \cup \{B\}$. By Definition 7, $\{C\} \cup [s'] \cup \{B\} \in [A \Rightarrow B \text{ in } \cdot]](prod(P))$.

 $s = A \cdot s'$. In this case we have the transition

 $\mathsf{A} \Rightarrow \mathsf{B} \text{ in } P \mathop{\to}\limits^{\mathsf{A}} P'$

Since the only applicable rule is Rule [**req2**], we obtain that there is $P_1 \in \text{SPLA}$ such that $P \stackrel{\text{\tiny A}}{\to} P_1 P' = P_1 \Rightarrow \text{B}$ and $s' \in \text{tr}(P_1 \Rightarrow \text{B})$. By Lemma 5, $[s'] \in [\![\rightarrow \Rightarrow B]\!] \text{prod}(P_1)$. By Definition 7, there is $s'' \in \text{tr}(P_1)$ such that $[s'] = [s''] \cup \{B\}$. Since $P \stackrel{\text{\tiny A}}{\to} P_1, \text{A} \cdot s'' \in \text{tr}(P)$. Therefore, by Definition 7,

$$\begin{split} p &= [s] = \{ \mathbb{A} \} \cup [s'] = \{ \mathbb{A} \} \cup [s''] \cup \{ \mathbb{B} \} \in \llbracket \mathbb{A} \\ \Rightarrow \mathbb{B} \text{ in } \cdot \rrbracket(\texttt{prod}(P)) \end{split}$$

Now we are going to prove

 $\llbracket A \Rightarrow B \text{ in } \cdot \rrbracket (\operatorname{prod}(P)) \subseteq \operatorname{prod}(A \Rightarrow B \text{ in } P)$

So let us consider $p \in [A \Rightarrow Bin \cdot](prod(P))$. By Definition 7 there are two cases:

- $A \notin p$. In this case $p \in prod(P)$, so there is $s \in tr(P)$ such that [s] = p. Since $A \notin P$, by applying rule [**req1**], $s \in tr(A \Rightarrow BinP)$.
- There is $p' \in \text{prod}(P)$ such that $A \in p'$ and $p = p' \cup \{B\}$. Let us consider $s \in \text{tr}(P)$ such that p' = [s]. So, there are traces s_1 and s_2 such that $s = s_1 \cdot A \cdot s_2$ and $A \notin s_1$. So, there exists P_1 and P_2 such that $P \Rightarrow P_1 \xrightarrow{A} P_2$ and $s_2 \in \text{tr}(P_2)$. Then, by applying rule [**req1**],

$$A \Rightarrow B \text{ in } P \stackrel{s_1}{\Rightarrow} A \Rightarrow B \text{ in } P_1 \stackrel{A}{\rightarrow} P_2 \Rightarrow B$$

Since $[s_2] \in \text{prod}(P_2)$, by Lemma 5, $[s_2] \cup \{B\} \in \text{prod}(P_2 \Rightarrow B)$. Let us consider $s'_2 \in \text{tr}(P_2 \Rightarrow B)$ such that $[s'_2] = [s_2] \cup \{B\}$. Therefore $s_1 \cdot A \cdot s'_2 \in \text{tr}(A \Rightarrow B \text{ in } P)$, so

$$\begin{split} p &= p' \cup \{ \mathbb{B} \} = [s_1] \cup \{ \mathbb{A} \} \cup [s_2] \cup \{ \mathbb{B} \} = [s_1] \cup \{ \mathbb{A} \} \cup [s'_2] \\ &\in \texttt{prod}(\mathbb{A} \Rightarrow \mathbb{B} \text{ in } P) \quad \Box \end{split}$$

Proof (Lemma 3.8). First we are going to prove

 $\operatorname{prod}(A \Rightarrow B \text{ in } P) \subseteq \llbracket A \Rightarrow B \text{ in } \cdot \rrbracket(\operatorname{prod}(P))$

So let us consider $p \in prod(A \Rightarrow Bin P)$. There exists a successful trace $s \in tr(A \Rightarrow Bin P)$ such that [s] = p. We will show that $p \in [A \Rightarrow Bin \cdot](prod(P))$ by induction on the length of *s*.

 $s = \epsilon$. In this case we have the transition

 $\operatorname{prod}(A \Rightarrow B \text{ in } P) \xrightarrow{\mu} \operatorname{nil}$

The only applicable rule is Rule [**excl4**]. So $P \stackrel{\checkmark}{\rightarrow} \text{nil}$ and therefore $\emptyset \in \text{prod}(P)$. So by Definition 7, we have $\emptyset \in [\mathbb{A} \Rightarrow \text{Bin} \cdot](\text{prod}(P))$. $s = \mathbb{C} \cdot s'$ with $\mathbb{C} \neq \mathbb{A}$ and $\mathbb{C} \neq \mathbb{B}$. In this case we have the transition $\mathbb{A} \Rightarrow \mathbb{B} \text{ in } P \stackrel{\frown}{\rightarrow} P'$. If $\mathbb{A} \neq \mathbb{C}$, because of Rule [**excl1**], we obtain $P' = \mathbb{A} \Rightarrow \mathbb{B} \text{in} P_1$ with $P \stackrel{\frown}{\rightarrow} P_1$, and $s' \in \text{tr}(\mathbb{A} \Rightarrow \mathbb{B} \text{in} P_1)$. By induction, we obtain $[s'] \in [\mathbb{A} \Rightarrow \mathbb{B} \text{ in } \cdot](\text{prod}(P_1))$. By Definition 7 there are two cases:

• $A \notin [S']$. In this case $[S'] \in \text{prod}(P_1)$. Therefore $\{C\} \cup [S'] \in \text{prod}(P)$. Since $A \notin \{C\} \cup [S']$,

$$p = \{C\} \cup [S'] \in \llbracket A \not\Rightarrow B \text{ in } \cdot \rrbracket (\text{prod}(P))$$

• $\mathbb{B} \notin [s']$. This case is identical to the previous one.

Now let us prove

 $[\![A \not\Rightarrow B \text{ in } \cdot]\!](\operatorname{prod}(P)) \subseteq \operatorname{prod}(A \not\Rightarrow B \text{ in } P)$

Let us consider $p \in [A \Rightarrow Bin \cdot](prod(P))$. There are two cases

- $A \notin p$. In this case $p \in prod(P)$. So there exists a trace $s \in tr(P)$ such that p = [s]. We prove this by induction on the length of *s*.
- $s = \epsilon$. In this case we have the transition $P \xrightarrow{\leftarrow} nil$. Then by Rule [excl4], $A \Rightarrow B \text{ in } P \xrightarrow{\leftarrow} nil$. So $p = \emptyset \in \text{prod}$ $(A \Rightarrow B \text{ in } P)$
- $s = \mathbb{C} \cdot s'$. Now we have two possibilities: $\mathbb{C} = \mathbb{B}$ or $\mathbb{C} \neq \mathbb{B}$. In the first case we have the transition $P \xrightarrow{\mathbb{B}} P_1$ with $s' \in \operatorname{tr}(P_1)$. By Rule [**excl3**], we obtain the transition $\mathbb{A} \Rightarrow \mathbb{B}$ in $P \xrightarrow{\mathbb{B}} P_1 \setminus \mathbb{A}$. On the one hand $[s'] \in \operatorname{prod}(P_1)$ and on the other hand $\mathbb{A} \notin [s'] \subseteq p$, so $[s'] \in [\mathbb{L} \setminus \mathbb{A}](P_1)$. Because of Lemma 6 $[s'] \in \operatorname{prod}(P_1 \setminus \mathbb{A})$, and then

$$p = \{C\} \cup [s'] = \{B\} \cup [s'] \in \operatorname{prod}(A \not\Rightarrow B \text{ in } P)$$

If $C \neq B$ we have the transition $P \stackrel{\circ}{\to} P_1$ with $s' \in tr(P_1)$. By Rule [**excl1**], we obtain the transition $A \neq B$ in $P \stackrel{\circ}{\to} A \neq B$ in P_1 . Since $A \notin [s']$, we obtain $[s'] \in [A \neq Bin \cdot](prod(P_1))$ By induction we obtain $[s'] \in prod(A \neq BinP_1)$, so

$$p = \{C\} \cup [S'] \in \text{prod}(A \not\Rightarrow B \text{ in } P)$$

B ∉ p. This case is similar to the previous one. The only difference appears in the inductive case when making the possibilities of C. In this case the possibilities are: C = A and C ≠ A. The second possibility is like the second possibility in the previous case. The difference with respect to the first possibility is that we need to apply Rule [excl2] instead of Rule [excl3]. □ **Proof** (Theorem 1). We prove this by using structural induction on *P* taking into account the previous lemmas. The base cases are P = nil and $P = \checkmark$. In this case it is easy to check that $\text{prod}(\text{nil}) = [[\text{nil}]] = \emptyset$ and $\text{prod}(\checkmark) = [[\checkmark]] = \{\emptyset\}$. The induction corresponds to

P = A; P' This case corresponds to Lemma 3.1. $P = \overline{A}; P'$ This case corresponds to Lemma 3.2. $P = P_1 \lor P_2$ This case corresponds to Lemma 3.3. $P = P_1 \land P_2$ This case corresponds to Lemma 3.4. $P = A \Rightarrow BinP$ This case corresponds to Lemma 3.7. $P = A \Rightarrow BinP$ This case corresponds to Lemma 3.8. $P = P \land A$ This case corresponds to Lemma 3.6. $P = P \Rightarrow A$ This case corresponds to Lemma 3.5. \Box

Proof (Corollary 1). We just need to consider the following:

 $\begin{aligned} \operatorname{prod}(\operatorname{op}(P_1,\ldots,P_n)) &= [\![\operatorname{op}(P_1,\ldots,P_n)]\!] = [\![\operatorname{op}]\!]([\![P_1]\!],\ldots,[\![P_n]\!]) \\ \operatorname{Since} P_i &\equiv Q_i, [\![P_i]\!] = \operatorname{prod}(P_i) = \operatorname{prod}(Q_i) = [\![Q_i]\!]. \text{ Thus} \\ [\![\operatorname{op}]\!]([\![P_1]\!],\ldots,[\![P_n]\!]) &= [\![\operatorname{op}]\!]([\![Q_1]\!],\ldots,[\![Q_n]\!]) = [\![\operatorname{op}(Q_1,\ldots,Q_n)]\!] \\ &= \operatorname{prod}(\operatorname{op}(Q_1,\ldots,Q_n)) \quad \Box \end{aligned}$

Proof (Proposition 9). Let us consider $A, B, C, D \in \mathcal{F}$. If $A \neq D$ it is easy to check, by using the definition of the *require* operator in Definition 7, the following

 $\llbracket \texttt{A} \Rightarrow \texttt{B} \text{ in } \texttt{C} \Rightarrow \texttt{D} \text{ in } P \rrbracket = \llbracket \texttt{C} \Rightarrow \texttt{D} \text{ in } \texttt{A} \Rightarrow \texttt{B} \text{ in } P \rrbracket$

Instead if A = D, because the term is closed under the *require* constraint, again by using the definition of the *require* operator:

 $\begin{bmatrix} A \Rightarrow B \text{ in } C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } P \end{bmatrix} = \\ \begin{bmatrix} A \Rightarrow B \text{ in } C \Rightarrow A \text{ in } C \Rightarrow B \text{ in } P \end{bmatrix} = \\ \begin{bmatrix} C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } C \Rightarrow B \text{ in } P \end{bmatrix} = \\ \begin{bmatrix} C \Rightarrow A \text{ in } C \Rightarrow B \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \begin{bmatrix} C \Rightarrow B \text{ in } A \Rightarrow B \text{ in } C \Rightarrow A \text{ in } P \end{bmatrix} = \\ \begin{bmatrix} C \Rightarrow B \text{ in } A \Rightarrow B \text{ in } C \Rightarrow A \text{ in } P \end{bmatrix} = \\ \\ \begin{bmatrix} C \Rightarrow B \text{ in } A \Rightarrow B \text{ in } C \Rightarrow A \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} = \\ \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \Rightarrow B \text{ in } P \end{bmatrix} \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \text{ in } B \text{ in } P \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \text{ in } B \text{ in } P \end{bmatrix} \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \text{ in } B \text{ in } P \end{bmatrix} \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \text{ in } B \text{ in } P \end{bmatrix} \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \text{ in } B \text{ in } P \end{bmatrix} \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A \text{ in } B \text{ in } P \end{bmatrix} \end{bmatrix} \begin{bmatrix} C \Rightarrow B \text{ in } C \Rightarrow B \text{ in } C \text{ in } C$

Let us prove in detail

 $\llbracket A \Rightarrow B \text{ in } C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } P \rrbracket \subseteq \llbracket C \Rightarrow B \text{ in } C \Rightarrow A \text{ in } A$ $\Rightarrow B \text{ in } P \rrbracket$

the other cases are similar. Any product $p \in [A \Rightarrow BinC \Rightarrow BinC \Rightarrow AinP]$ is built from a product $p' \in [P]$. We can distinguish the following cases:

$$C \in p'. \text{ Then } p = p' \cup \{A,B\} \text{ and } p \in [C \Rightarrow BinC \Rightarrow AinA \Rightarrow BinP]].$$

$$C \notin p', A \in p'. \text{ Then } p = p' \cup \{B\} \text{ and } p \in [C \Rightarrow BinC \Rightarrow AinA \Rightarrow BinP]].$$

$$C,A \notin p'. \text{ Then } p = p' \text{ and } p \in [C \Rightarrow BinC \Rightarrow AinA \Rightarrow BinP]].$$

Proof (Proposition 4). This proposition is immediate by the definition of the *exclude* operator in Definition 7. \Box

Proof (Theorem 2). Since \mathcal{F} is finite *A* is finite, so we can prove the result by induction on |A|.

|A| = 0 In this case the P = nil.

|A| > 0 Let us consider a product $p \in A$ and the set $A' = A \setminus \{p\}$. By induction there is $P' \in SPLA_b$ such that prod(P') = A'. Since \mathcal{F} is finite, p is a finite set of features $p = \{A_1, \ldots, A_n\}$. Then the term

$$P = (A_1; \ldots A_n; \mathbf{i}) \vee P'$$

satisfies the thesis of the result. \Box

Proof (Proposition 5). In all axioms but [**PRE1**], [**PRE4**], [**PRE5**], [**REQ3**], [**CON1**], [**CON4**] the transitions of the terms on both sides of the equation are the same. So in these cases the traces are the same, so they have the same products.

In the other cases the proof is done by using the denotational semantics.

$$[\mathbf{PRE1}] \quad A; B; P = {}_{E}B; A; P$$
$$\llbracket A; B; P \rrbracket = \{A\} \cup \llbracket B; P \rrbracket = \{A\} \cup \{B\} \cup \llbracket P \rrbracket = \{B\} \cup \{A\} \cup \llbracket P \rrbracket$$
$$= \{B\} \cup \llbracket A; P \rrbracket = \llbracket B; A; P \rrbracket$$

[**PRE4**] A;nil = _Enil

$$p \in \llbracket A; \texttt{nil} \rrbracket \iff \exists p' \in \llbracket \texttt{nil} \rrbracket \land p = p' \cup \{A\} \iff \texttt{False} \iff p \in \llbracket \texttt{nil} \rrbracket$$

[**PRE5**] $A;A;P = {}_{E}A;P$

 $\llbracket \mathsf{A}; \mathsf{A}; P \rrbracket = \{\mathsf{A}\} \cup \llbracket \mathsf{A}; P \rrbracket = \{\mathsf{A}\} \cup \{\mathsf{A}\} \cup \llbracket P \rrbracket = \{\mathsf{A}\} \cup \llbracket P \rrbracket$ $= \llbracket \mathsf{A}; P \rrbracket$

[REQ3] $A \Rightarrow Bin (B;P) = {}_{E}B;P$. In this case it is enough to consider that $B \in [\![B];P]\!]$. Therefore by the Definition 7,

 $p \in \llbracket A \Rightarrow B \text{ in } \cdot \rrbracket(\llbracket B; P \rrbracket) \iff p \in \llbracket B; P \rrbracket$

[CON 1] (A;P) \land Q = _EA; (P \land Q)

$$\begin{split} p \in \llbracket (\mathbb{A}; P) \land \mathbb{Q} \rrbracket & \Longleftrightarrow \\ \exists p_1 \in \llbracket \mathbb{A}; P \rrbracket, \ p_2 \in \llbracket \mathbb{Q} \rrbracket : p = p_1 \cup p_2 & \Longleftrightarrow \\ \exists p'_1 \in \llbracket P \rrbracket, \ p_2 \in \llbracket \mathbb{Q} \rrbracket : p = p'_1 \cup \{\mathbb{A}\} \cup p_2 & \Longleftrightarrow \\ \exists p' \in \llbracket P \land \mathbb{Q} \rrbracket : p = p' \cup \{\mathbb{A}\} & \longleftrightarrow \\ p \in \llbracket \mathbb{A}; (P \land \mathbb{Q}) \rrbracket \end{split}$$

[CON 4] $P \land \text{nil} = E \text{nil}$

$$\begin{split} p \in \llbracket P \land \texttt{nil} \rrbracket &\iff \\ \exists p_1 \in \llbracket P \rrbracket, p_2 \in \llbracket \texttt{nil} \rrbracket : p = p_1 \cup p_2 &\iff \\ \exists p_1 \in \llbracket P \rrbracket, p_2 \in \emptyset : p = p_1 \cup p_2 &\iff \\ \mathsf{False} &\Longleftrightarrow p \in \llbracket \texttt{nil} \rrbracket \quad \Box \end{split}$$

Proof (Theorem 3). The proof is made trivially by structural induction on *P*. \Box

Proof Lemma 4. The proof is made by induction on the depth of *P*. Let us note that each P_i term appearing in part 2 of Definition 13 cannot be nil. \Box

Proof (Lemma 5). The proof is made by structural induction of *P* by applying Axioms [**PRE 1**] and [**PRE 3**]. \Box

Proof Proposition 6. The proof is made by structural induction of P and applying Lemma 5. \Box

Proof (Proposition 7). The proof is made by induction on the depth of the pre-normal form P. The base case is trivial because $P = \nu$ or P = nil and in these cases P are already in normal form.

The inductive case needs further explanations. The difference between a normal form and a pre-normal form is the ordering imposed to the features. Basically there are two cases. The first case is when *P* has the following form:

$$P = \ldots \lor B; P \lor \ldots \lor A; Q \lor \ldots$$
 with $A < B$

This term can be transformed into normal form by applying the commutativity of the choose-one operator (Eq. [CHO 1]).

The second case is when the features that are not properly ordered appear in the same subterm:

$$P = \ldots \lor B; (\ldots A; P \ldots) \lor \ldots$$
 with $A < B$

This case can be transformed by applying the Eqs. [PRE 3] and [PRE 1) as indicated below:

$$B; ((A; P) \lor Q) = E \qquad [PRE3]$$

$$(B; A; P) \lor (B; Q) = E \qquad [PRE1]$$

$$(A; B; P) \lor (B; Q) \qquad \Box$$

Proof (Proposition 8). Let us make an argument by contradiction. Let us suppose we have $P, Q \in SPLA_{nf}$ and let us suppose that they are syntactically different and let us prove that they are not equivalent, $P \neq Q$. The proof is made by induction on the depth of *P* and *Q*. The base case is when both are either \nvdash or nil and the result is trivial. In the inductive case we have the following possibilities:

(a) $P = (A_1; P_1) \vee (A_m; P_n),$ (b) $P = (A_1; P_1) \lor (A_m; P_n) \lor \checkmark$ (c) $Q = (B_1; Q_1) \vee (B_m; Q_m)$, or (d) $\mathbf{Q} = (\mathbb{B}_1; \mathbf{Q}_1) \lor (\mathbb{B}_m; \mathbf{Q}_m) \lor \boldsymbol{\checkmark}$

If we are in case (a) + (d) we $\emptyset \in \text{prod}(P)$ but $\notin \text{prod}(Q)$, so $P \neq Q$. The case (b)+(c) is symmetric. Let us consider case (a) + (c), the case (b) + (d) is solved in the same way. If P and Q are syntactically different then there are two possibilities:

- $\{A_1, \ldots, A_n\} \neq \{B_1, \ldots, B_m\}$. Let us consider the first difference among both sets. Let us assume that the first one consist in an element from the first set that is not in the second set, that is, there is $k \in \{1, ..., n\}$ such that $A_k \notin \{B_1, ..., B_m\}$ and $A_i = B_i$ for *i* < *k*. There are the following possibilities:
 - k = m + 1 Any occurrence of A_k in Q should be in any of the sub-trees Q_j , j < k. By Lemma 4, that would imply that $\mathbb{B}_i \in p$ for any product $p \in prod(Q)$ such that $A_k \in P$. But $B_i < A_k$, then $B_i \notin voc(P_k)$. Therefore there are products $p' \in \text{prod}(P)$ such that $\mathbb{B}_i \notin p'$. Therefore $P \neq 0$.
 - $A_k < B_k$ This case is similar to the previous one because $A_k \neq B_l$ for $l \ge k$ and $A_k \notin \text{voc}(Q_l)$ for $l \ge k$.

 $A_k > B_k$ This case is symmetric to the previous one.

• n = m, $A_i = B_i$ for $1 \leq i \leq n$, and $\{P_1, \dots, P_n\} \neq \{Q_1, \dots, Q_n\}$. Let us consider the first k such that $P_k \neq Q_k$. By structural induction we have $P_k \neq Q_k$, let us assume that $p \in \text{prod}(P_k)$ but $p \notin \operatorname{prod}(Q_k)$. It is clear that $p \cup \{A_k\} \in \operatorname{prod}(P)$, we are going to prove that $p \cup \{A_k\} \notin prod(Q)$. On the one hand, since $A_k \notin \operatorname{voc}(Q_i)$ for $i \ge k$, by Lemma 4, $p \cup \{A_k\} \notin \operatorname{prod}(A_i;Q_i)$ for $i \ge k$. On the other hand, since $A_i \notin voc(P_k)$ for i < k, again by Lemma 4, $A_i \notin p$ and thus $A_i \notin p \cup \{A_k\}$ for i < k. Since $A_i \in q$ for any $q \in \operatorname{prod}(A_i; Q_i)$, $p \cup \{A_k\} \notin \operatorname{prod}(A_i; Q_i)$ for i < k. Thus, for any $1 \leq i \leq n, p \cup \{A_k\} \notin prod(A_i; Q_i)$ and then $p \cup \{A_k\} \notin \operatorname{prod}(Q)$. Therefore $P \neq Q$.

Proof Theorem 4. We have to prove two implications

- If $P = P_{P,0}$ then $P \equiv Q$. This is a consequence of the soundness of each rule (Proposition 5).
- If then $P \equiv Q$ then $P = P_E Q$. By Proposition 6, there are P_{pre} , $Q_{pre} \in SPLA_{pre}$ such that $P_{pre} = E_P$ and $Q = E_{Pre}$. Now by Proposition 7, there are $P_{nf}, Q_{nf} \in SPLA_{nf}$ such that $P_{nf} = {}_{E}P_{pre} = {}_{E}P$ and $Q_{nf} = E_{Opre} = E_{O}$. By Proposition 5, we have

$$P_{nf} \equiv P_{pre} \equiv P \equiv Q \equiv Q_{pre} \equiv Q_{nf}$$

Finally, by Proposition 8, we have that P_{nf} and Q_{nf} are identical so P = E O.

Proof (Lemma 6).

- 1. By construction of $\varphi_1^{\Rightarrow \varphi_2}$.
- 2. It is enough to consider the valuation v' defined as:

$$\nu'(\mathbb{A}_l) = \begin{cases} \nu(\mathbb{A}_k) & \text{if } 0 \leqslant \mathsf{maxin}(\mathbb{A}_l, \varphi) < k \\ \nu(\mathbb{A}_l) & \text{otherwise} \end{cases}$$

3. By construction of $\varphi_1^{\Rightarrow \varphi_2}$. \Box

Proof (Lemma 7). The proof will be done immediate by structural induction on P.

- $P = \text{nil or } P = \mathbf{i}$. Trivial since $\operatorname{vars}(\phi(P)) = \emptyset$. $P = \mathbb{B}$; P'. In this case $\phi(P) = \mathbb{B}_{m+1} \land \phi(P')$ where $m = \max(\mathbb{B}, \phi(P))$. In this case $\mathbb{A}_l \neq \mathbb{B}_{m+1}$. So either $A \neq B$ or A = B and $l \neq m + 1$. In the second case we can deduce $l \leq m$, because of the definition of maxin. So in both cases $A_l \in vars(\phi(P'))$ so we obtain the result by structural induction. $P = \overline{\mathbb{B}}; P'$. Trivial since vars $(\phi(P)) = \emptyset$.
 - $P = P_1 \vee P_2$ In this case $\phi(P) = \phi(P_1)^{\Rightarrow \phi(P_2)} \vee \phi(P_1)^{\Rightarrow \phi(P_2)}$. Since $v \models \phi(P)$, then $v \models \phi(P_1)^{\Rightarrow \phi(P_2)}$ or $v \models \phi(P_2)^{\Rightarrow \phi(P_1)}$. Let assume the first case. Since $\nu \models \phi(P_1)^{\Rightarrow \phi(P_2)}$, $\nu \models \phi(P_1)$. Then we have the following cases

 $A_l \in vars(\phi(P_1))$ Then we obtain the result by induction on P_1 .

 $A_l \notin vars(\phi(P_1))$. Let consider $m = maxin(A, \phi(P_1))$. Then, by Lemma 6.3, $m < l \leq \max(A, \phi(P_2))$. Since $v(A_l) = 0$ and $v \models \phi(P_2)^{\Rightarrow \phi(P_1)}$, by construction of $\phi(P_2)^{\Rightarrow \phi(P_1)}$, we obtain $v(A_k) = 0$ for $m \le k \le l$. Therefore $v(A_m) = 0$ and then, by induction on P_1 , we obtain the result.

 $P = P_1 \wedge P_2$. This case is done directly by structural induction. $P = B \Rightarrow C in P'$

or $P = B \Rightarrow C \operatorname{in} P'$.

If $A \neq B$ or $A \neq C$ the result is obtained by applying directly structural induction. Let us assume that A = B (the case when A = C is identical changing B by C). Let us consider $m = \max(B, \phi(P'))$. If $l \leq m$ then $A_l \in vars(\phi(P'))$ and we obtain the result by structural induction. So it remains the case when l = m + 1. If l = 0 there is nothing to prove, so let us assume l > 0. Since $v \models \phi(P)$ we obtain $\nu \models \neg A_{m+1} \rightarrow A_m$. Since $\nu(A_{m+1}) = 0$ then m = -1 or $\nu(A_m) = 0$, we us assume $m \ge 0$ because if m = -1 there is nothing left to prove. Because of the definition of maxin, $A_m \in vars(\phi(P'))$. Then, by structural induction, $v(A_k) = 0$ for $k \leq m = l - 1$ that completes the result. $P = P' \Rightarrow B$. This case is the same as P = A; P.

 $P = P' \setminus \mathbb{B}$. This case is done directly by structural induction. \Box

Proof (Proposition 9). The proof will be done by structural induction of *P*. In all cases we are going to use Theorem 1: prod(P) = [P].

 $P = \text{nil or } P = \checkmark$. These are the base cases, and it is immediate. ϕ (nil) is not satisfiable and $\text{prod}(\text{nil}) = \emptyset$. While the valuation v such that $v(A_k) = 0$ for any $A \in \mathcal{F}$ and $k \in \mathbb{N}$ meets the thesis for $P = \checkmark$.

P = A; P'. In this case $\phi(A; P') = A_{l+1} \land \phi(P')$ where $l = \max (n(A, \phi(P')))$. Let us consider $p \in \operatorname{prod}(P)$, then there is $p' \in \operatorname{prod}(P')$ such that $p = \{A\} \cup p'$. By applying induction on P' there is a valuation v' that meet the thesis for p' and P'. Let us consider the valuation $v = v'[A_{l+1}/1]$.⁷ It is easy to check that v holds the thesis for p and P:

- $\nu \vDash A_{l+1} \land \phi(P')$ since $A_{l+1} \notin vars(\phi(P'))$.
- Let us consider $\mathbb{B} \in p$. Then $\mathbb{B} = \mathbb{A}$ or $\mathbb{B} \in p'$. In the first case $\iota(\mathbb{A}_{l+1}) = 1$. In the second case, by induction, $k \ge 0$ and $\nu'(\mathbb{B}_k) = 1$ where $k = \max(\mathbb{B}, \phi(P'))$. Then $\nu'(\mathbb{B}_k) = 1$ and $k = \max(\mathbb{B}, \phi(P)) = \max(\mathbb{B}, \phi(P'))$.
- Now let us consider $\mathbb{B} \notin p$. Then $\mathbb{B} \neq \mathbb{A}$ and $\mathbb{B} \notin p'$. By induction, k = -1 or $\nu'(\mathbb{B}_k) = 0$ where $k = \max(\mathbb{B}, \operatorname{vars} \phi(P'))$. Then $k = \max(\mathbb{B}, \operatorname{vars} \phi(P))$ and k < 0 or $\nu(\mathbb{B}_k) = 0$.
- $P = P_1 \lor P_2$. In this case $prod(P) = prod(P_1) \cup prod(P_2)$. Therefore $p \in prod(P)$ iff $p \in prod(P_1)$ or $p \in prod(P_2)$. Let us suppose $p \in prod(P_1)$ (the other case is symmetric). By induction there is a valuation v satisfying the thesis for p and P_1 . Now let us consider the valuation v' defined in the proof of Lemma 6.2. Let us prove that v' meets the thesis.
- Since $\nu \vDash P_1$, then $\nu' \models \phi(P_1)^{\Rightarrow \phi(P_2)}$.
- Now let us consider a feature $A \in p$. By induction $v(A_k) = 1$ where $k = \max(A, \phi(P_1))$. Then $v'(A_k) = 1$ by construction of v'. If $k = \max(A, \phi(P))$ we have the result. So, let us suppose $k < \max(A, \phi(P))$, let $m = \max(A, \phi(P))$. Since $v' \models \phi(P)$, if $v(A_m) = 0$, by Lemma 7, we could conclude $v(A_k) = 0$, therefore $v(A_m) = 1$.
- Finally let us consider $A \notin p$. By induction m = -1 or $\iota(A_m) = 0$ where $m = \max(A, \phi(P_1))$. If $m = \max(A, \phi(P))$, by construction of ν' , $\nu'(A_m) = 0$. If $m < \max(A, \phi(P))$, then let us consider $l = \max(A, \phi(P_2)) = \max(A, \phi(P_1)^{\Rightarrow \phi(P_2)})$. By construction of $\nu', \nu'(A_l) = \iota(A_m) = 0$.
- $P = \overline{\mathbb{A}}; P'$. This case is a particular case of the previous one since $P = {}_{E} \nu \vee \mathbb{A}; P'$.
- $P = P_1 \land P_2$. In this case $p \in prod(P)$ iff there is $p_1 \in prod(P_1)$ and $p_2 \in prod(P_2)$ such that $p = p_1 \cup p_2$. By induction on P_1 and P_2 , there is a valuation v_1 holding the thesis for p_1 and P_1 and v_2 holding the thesis for p_2 and P_2 . Let us consider the valuation v defined as

$$\nu(x) = \begin{cases} 1 & \text{if } \nu_1(x) = 1 \text{ and } x \in \operatorname{vars}(\phi(P_1)) \\ 1 & \text{if } \nu_2(x) = 1 \text{ and } x \in \operatorname{vars}(\phi(P_2)) \\ 0 & \text{othewise} \end{cases}$$

This valuation holds the thesis for *p* and *P*:

- Since *P* is safe and the only operators that introduce negated boolean variables are the restrictions, there are no negated boolean variables in $\phi(P_1)$ or in $\phi(P_2)$. Therefore $\nu \models \phi(P) = \phi(P_1) \land \phi(P_2)$.
- Let us consider a feature $A \in p$. Then $A \in p_1$ or $A \in p_2$. Let us assume $A \in p_1$, the other case is symmetric. Then, by induction, $v_1(A_l) = 1$ where $l = \max(A, \phi(P_1))$. Then, by construction of v, $\iota(A_l) = 1$ Let us consider $m = \max(A, \phi(P))$. If $\iota(A_m) = 0$, by Lemma 7 $\iota(A_l) = 0$, so $\iota(A_m) = 1$
- Let us consider $A \notin p$. Then $A \notin p_1$ and $A \notin p_1$ Let us consider $l = \max(A, \phi(P))$. then $l = \max(A, \phi(P_1))$ or $l = \max(A, \phi(P_2))$. Let us suppose $l = \max(A, \phi(P_1))$, the other case is symmetric. Since $A \notin p_1$, l = -1 or $v_1(A) = 0$. There two possibilities $l = \max(A, \phi(P_2))$ or $l < \max(A, \phi(P_2))$. In the second case $A_l \notin vars(\phi(P_2))$ so, by construction of $v, v(A_l) = 0$. In the first case, by induction, l = -1 or $v_2(A_l) = 0$; so, by construction of $v, v(A_l) = 0$.
- $$\begin{split} P = \mathbb{A} \Rightarrow \mathbb{B} \text{in} P'. & \text{In this case } \phi(P) = (\neg \mathbb{A}_{l+1} \to \neg \mathbb{A}_l) \land (\neg \mathbb{B}_{m+1} \to \neg \mathbb{B}_m) \land (\mathbb{A}_{l+1} \to \mathbb{B}_{m+1}) \land \phi(P') \text{ where } l = \max(\mathbb{A}, \phi(P')) \text{ and } m = \max(\mathbb{B}, \phi(P')). \\ & \text{Let us consider } p \in \operatorname{prod}(P) \text{ iff there is } p' \in \operatorname{prod}(P') \text{ such that } p = p' \text{ and } \mathbb{A} \notin p' \text{ or } p = p' \cup \{\mathbb{B}\} \text{ and } \mathbb{A} \in p'. \text{ In both cases, by induction, } \\ & \text{there is a valuation } v' \text{ satisfying the thesis for } p' \text{ and for } P'. \end{split}$$

Let us consider the valuation *v* according to the following cases:

A ∈ p'. Then $v = v'[A_{l+1}/1, B_{m+1}/1]$. A ∉ p' and B ∈ p'. Then $v = v'[A_{l+1}/0, B_{m+1}/1]$. A ∉ p' and B ∉ p'. Then $v = v'[A_{l+1}/0, B_{m+1}/0]$.

It is easy to check that all three cases *v* satisfy the conditions for *p* and *P*:

- Due to the way v and $\phi(P)$ are defined, $v \vDash \phi(P)$.
- Let us consider $C \in p$. If C = B there are two cases $B \in p'$ or $A \in p'$; in both cases, by construction of $v, v(C_{m+1}) = 1$. If C = A then $A \in p'$, so $v(A_{l+1}) = 1$. If $C \neq B$ and $C \neq B$, then $C \in p'$ and by induction $l \ge 0$ and $v'(C_l) = v(C_l) = 1$ for $l = maxin(C, \phi(P')) =$ $maxin(C, \phi(P))$
- Let us consider $C \notin p$. Then $C \notin p'$. By induction l < 0 or $\nu'(C_l) = 0$ for $l = \max(C, \phi(P'))$. If $C \neq A$ or $C \neq A$, then $l = \max(C, \phi(P'))$ and l < 0 or $\nu(C_l) = \nu'(C_l) = 0$. If C = B, then $B \notin p'$ and, by construction of $v, \nu(B_{m+1}) = 0$. If C = A, then $A \notin p'$ and, by construction of v, $\nu(A_{l+1}) = 0$
- $P = \mathbb{A} \Rightarrow \mathbb{B} \text{in} P'$. In this case $\phi(P) = (\neg A_{l+1} \rightarrow \neg A_l) \land (\neg B_{m+1} \rightarrow \neg B_m) \land (\neg A_{l+1} \lor \neg B_{m+1}) \land \phi(P')$ where $l = \max(A, \phi(P))$ and $m = \max(B, \phi(P))$. Moreover, $p \in \text{prod}(P)$ iff $p \in \text{prod}(P')$ and $A \notin p$ or $B \notin p$. Let us assume the first case (the other is symmetric). By induction hypothesis there is a valuation ν' satisfying the thesis for p and P'. Now there are two cases $B \in p$ or $B \notin p$, in the first case let us consider $\nu = \nu'[A_{l+1}/0, B_{m+1}/1]$, in the second case let us consider $\nu = \nu'[A_{l+1}/0, B_{m+1}/0]$. Let us check that ν satisfies the thesis for p and P:
- Let us check that $v \vDash \phi(P)$ by analyzing its parts. Since $A \notin p$, by induction, l < 0 or $\iota(A_l) = 0$, so $v \vDash \neg A_{l+1} \rightarrow \neg A_l$. Also by induction, $B \in p$ iff $B \in p'$ iff $m \ge 0$ and $v'(B_m) = 1$. Therefore $v \vDash \neg B_{m+1} \rightarrow \neg B_m$. By construction $v \vDash \neg A_{l+1} \lor \neg B_{m+1}$ and by induction $v' \vDash \phi$ (*P*'). Since $A_{l+1}.B_{m+1} \notin vars(\phi(P')), v \vDash \phi(P')$.
- Let us consider $C \in p$, since we are considering $A \notin p', C \neq A$. If C = B, then $B \in p'$. Therefore, by construction of v, $v(B_{m+1}) = 1$.

⁷ v[A/x](B) = v(B) if $A \neq B$ and v[A/x](A) = x.

Otherwise by induction hypothesis, $k \ge 0$ and $\nu'(C_k) = 1$ for $k = \max(C, \phi(P'))$. Then $k = \max(C, \phi(P))$ and $\nu(C_k) = \nu'(C_k) = 1$.

• Let us consider $C \notin p$. If C = A then $\nu(A_{l+1}) = 0$. If C = B then $\nu(B_{m+1}) = 0$. If $C \neq A$ and $C \neq B$, by induction k < 0 or $\nu'(C_k) = 0$ for $k = \max(C, \phi(P))$. Let us observe that $k = \max(C, \phi(P))$ and $\nu(C_k) = \nu'(C_k) = 0$

 $P = P' \Rightarrow A$. This case is the same as P = A; P.

- $P = P' \setminus A$. In this case $\phi(P) = \neg A_l \land \phi(P)$ where $l = \max(A, \phi(P'))$. Let us consider $p \in \operatorname{prod}(P)$, then there is $p \in \operatorname{prod}(P')$ such that $A \notin p$. By structural induction there is a valuation v holding the thesis for p and P'. Let us check that v also satisfies the conditions for P.
- First $v \vDash \phi(P)$. There are two cases: l = -1 or $l \ge 0$. In the first case, the A_{l-1} is the symbol \bot . In the second case, since $A \notin p$, by induction $v(A_l) = 0$. In any case, by induction hypothesis, $v \vDash \neg A_l \land \phi(P')$.
- Let us consider a feature B. By induction, $B \in p$ iff l > 0 and $\iota(B_l) = 1$ where $l = \max(B, \phi(P'))$. To conclude this item it is enough to consider that $vars(\phi(P)) = vars(\phi(P'))$.

The second condition holds trivially since $vars(\phi(P)) = vars(\phi(P))$. \Box

Proof Proposition 10. The proof will be done by structural induction on *P*.

> P = A;P'. In this case $\phi(A;P') = A_{l+1} \land \phi(P')$ where $l = \max$ (0,maxin($A, \phi(P')$)). If $v \models \phi(P)$, then $v \models \phi(P')$. By structural induction there is $p' \in \operatorname{prod}(P')$, satisfying the thesis. Let us consider $p = p' \cup \{A\} \in \operatorname{prod}(P)$. By the definition of the denotational semantics of the prefix operator, $p \in \operatorname{prod}(A;P')$.

> > Now let us consider a feature C such that $\iota(C_k) = 0$ for all $0 \le k \le \max(C, \phi(P))$. Since $\nu \models \phi(P)$, $\iota(A_{l+1}) = 0, C \ne A$. By induction, $C \notin p'$, then, by construction of $p, C \notin p$.

- $P = P_1 \lor P_2.$ In this case $\phi P = \phi(P_1)^{\Rightarrow \phi(P_2)} \lor \phi(P_2)^{\Rightarrow \phi(P_1)}.$ Since $v \models \phi(P)$, then $v \models \phi(P_1)^{\Rightarrow \phi(P_2)}$ or $v \models \phi(P_2)^{\Rightarrow \phi(P_1)}.$ Let us suppose $v \models \phi(P_2)^{\Rightarrow \phi(P_1)}$ (the other case is symmetrical). By Lemma 6.1, $v \models \phi P_2$. Then, by induction, there is a product $p \in \text{prod}(P_2)$ that meet the thesis for p and P_2 . Since $\text{prod}(P) = \text{prod}(P_1) \cup \text{prod}(P_2)$, $p \in \text{prod}(P).$ Now let us consider a feature A such that $\iota(A_l) = 0$ for all $0 \le l \le \max(A, \phi(P_2))$, by induction, $A \le p$.
- $P = \overline{A}; P'$. This case is a particular case of the previous one since $P = {}_{E} \checkmark \lor A; P'$.
- $P = P_1 \land P_2$. In this case $\phi(P) = \phi(P_1) \land \phi(P_2)$. Since $v \models \phi(P)$ then $v \models \phi(P_1)$ and $v \models \phi(P_2)$. By structural induction there is $p_1 \in \text{prod}(P_1)$ that meet the thesis for v and P_1 and there is $p_2 \in \text{prod}(P_2)$ that meet the thesis for v and P_2 . Let us show $p = p_1 \cup p_2$ meet the thesis for v and P. First, by the definition of the operators $p \in \text{prod}(P)$. Now let us consider a feature A such that $v(A_1) = l$ for all $0 \le k \le \max(A, \phi(P))$. Since

 $\max(A,\phi(P)) \ge \min(A,\phi(P_1))$ and $\max(A,\phi(P_1))$ (P)) $\ge \max(A,\phi(P_2))$, by induction $A \notin p_1$ and $A \notin p_2$. Therefore $A \notin p_1 \cup p_2 = p$.

- $$\begin{split} P = \mathbb{A} \Rightarrow \mathbb{B} \text{in} P'. & \text{In this case } \phi(P) = (\neg \mathbb{A}_{l+1} \to \neg \mathbb{A}_l) \land (\neg \mathbb{B}_{m+1} \to \neg \mathbb{B}_m) \land (\mathbb{A}_{l+1} \to \mathbb{B}_{m+1}) \land \phi(P') & \text{where } l = \max_{in}(\mathbb{A}, \phi(P')) & \text{and } m = \max_{in}(\mathbb{B}, \phi(P')). & \text{Let us consider } v & \text{a valuation such that } v \vDash \phi(P). & \text{By induction, there is } p' \in \operatorname{prod}(P') & \text{holding the thesis for } v & \text{and } P. \\ & \text{There are two cases } \mathbb{A} \in p' & \text{or } \mathbb{A} \notin p'. & \text{In the first case let us consider } p = p' \cup \{\mathbb{B}\} \in \operatorname{prod}(P), & \text{and in the second case let } p = p'. & \text{Let us check that, } in both cases, p & \text{hold the thesis for } v & \text{and } P. & \text{By the definition of the denotational semantics, } p \in \operatorname{prod}(P). & \text{Now let us consider a feature } \mathbb{C} & \text{such that } \iota(\mathbb{C}_k) = 0 & \text{for all with } k \in \mathbb{N}. & \text{There are the following cases:} \end{split}$$
 - C = A. By induction $A \notin p'$. Then, by construction of p, $A \notin p$.
 - C = B. By induction B ∉ p'. Since $\iota(\mathbb{B}_{m+1}) = 0$ and $\nu \models \phi$ (*P*), $\iota(\mathbb{A}_{l+1}) = 0$. Then, l = -1 or $\iota(\mathbb{A}_l) = 0$. Then, by Lemma 7, $\iota(\mathbb{A}_k) = 0$ for all $0 \le k < l$. So, by induction, A ∉ p'. Since B ∉ p' and A ∉ p', by construction of *p*, B ∉ *p*.
- $C \neq B$ and $C \neq A$. In this case, by induction, $C \notin p'$. Since $C \neq B$ and $C \neq A$, by construction of $p, C \notin p$.
- $P = \mathbb{A} \Rightarrow \mathbb{B} \text{ in } P'$. In this case $\phi(P) = (\neg \mathbb{A}_{l+1} \rightarrow \neg \mathbb{A}_l) \land (\neg \mathbb{B}_{m+1} \rightarrow \neg \mathbb{A}_l)$ $\neg B_m$) \land ($\neg A_{l+1} \lor \neg B_{m+1}$) $\land \phi(P')$ where $l = maxin(A, \phi)$ (P')) and $m = \max(\mathbb{B}, \phi(P'))$. Let us consider v such that $v \models \phi(P)$. Then $v \models \phi(P')$, and by induction, there is $p \in prod(P')$ that holds the thesis for v and P'. Let us show that p also holds the thesis for v and P. First we will prove that $p \in prod(P)$. Since $v \vDash \phi(P)$, $v(A_{l+1}) = 0$ or $\iota(\mathbb{B}_{m+1}) = 0$; let us assume $\iota(\mathbb{B}_{m+1}) = 0$, the other case is symmetric. Since $v \models \phi(P)$, m = -1 or $\iota(\mathbb{B}_m) = 0$, then, by Lemma 7, $\iota(\mathbb{B}_k) = 0$ for $0 \leq k \leq m$. Then, by induction, $\mathbb{B} \notin p$. So, by the definition of the denotational semantics, $p \in prod(P)$. Now let us consider a feature C such that $v(C_k) = 0$ for all $0 \le k \le maxin(C,\phi(P))$. There are the following cases:
 - C = A. Then k = l + 1 and $l = \max(A, \phi(P'))$. Since $\nu \models \phi(P)$ and $\iota(A_{l+1}) = 0, l = -1$ or $\iota(A_l) = 0$. Then, by Lemma 7, $\iota(A_k) = 0$ for $0 \le k \le l$. Therefore, by induction, $A \notin p$.
 - C = B. Since $\nu \models \phi(P)$ and $\iota(\mathbb{B}_{m+1}) = 0, m = -1$ or $\iota(\mathbb{B}_m) = 0$. Then by Lemma 7, $\iota(\mathbb{B}_k) = 0$ for $0 \le k \le m$. Then, by induction $\mathbb{B} \notin p$.
- $\mathbb{C} \neq \mathbb{B}$ and $\mathbb{C} \neq \mathbb{A}$. In this case it is enough to consider that maxin($\mathbb{C}, \phi(P')$) = maxin($\mathbb{C}, \phi(P)$). Then, by induction, $\mathbb{C} \notin p$.
- $P = P' \Rightarrow A$. This case is the same as P = A; P.
- $P = P' \setminus A$. In this case $\phi(P) = \neg A_l \land \phi(P')$ where $l = \max(A, \phi(P'))$. Let us consider a valuation v such that $v \models \phi(P)$. Then $v \models \phi(P')$. By induction there is $p \in \operatorname{prod}(P')$ holding the thesis for v and P'. Let us check that p holds the thesis for v and P. First, let us show that $A \notin p$. Since $v \models \phi(P)$, there are two cases l = -1 or $l \ge 0$ and $v(A_l) = 0$. Then, by Lemma 7, $v(A_k) = 0$ for $0 \le k \le l$.

Then, by induction, $A \notin p$. So, by the definition of the semantic operator $[\cdot A], p \in prod(P)$. Now let us consider a feature C such that $v(C_k) = 0$ for $0 \leq k \leq maxin(C, \phi(P))$. We have already proved that $A \notin p$, so we can assume that $C \neq A$. Since $maxin(C, \phi(P)) = maxin(C, \phi(P))$, we obtain $C \notin P$ by induction. \Box

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